

SEMiX® 3p shunt

Trench IGBT Modules

SEMiX453GB17E4I50p

Features*

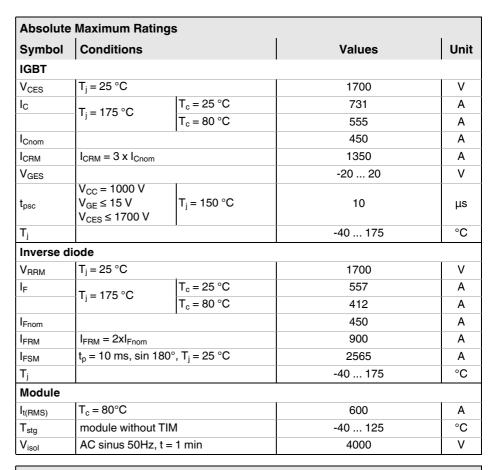
- · Homogeneous Si
- Trench = Trenchgate technology
- V_{CE(sat)} with positive temperature coefficient
- · High short circuit capability
- · Press-fit pins as auxiliary contacts
- · Current sensing shunt resistor
- UL recognized, file no. E63532

Typical Applications

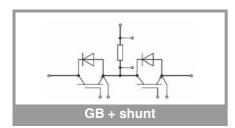
- · AC inverter drives
- UPS
- Renewable energy systems

Remarks

- Product reliability results are valid for T_i=150°C
- V_{isol} between temperature sensor and power section is only 2500V
- For storage and case temperature with TIM see document "TP(*) SEMiX 3p"



| Characteristics | | | | | | | |
|--|--|------------------------------|------|-------|------|-----|--|
| Symbol | Conditions | min. | typ. | max. | Unit | | |
| IGBT | | | | | | • | |
| V _{CE(sat)} | $\begin{array}{c} V_{\text{CE(sat)}} \\ \hline V_{\text{GE}} = 450 \text{ A} \\ V_{\text{GE}} = 15 \text{ V} \\ \text{chiplevel} \end{array}$ | T _j = 25 °C | | 1.90 | 2.20 | V | |
| | | T _j = 150 °C | | 2.26 | 2.45 | V | |
| V _{CE0} | chiplevel | T _j = 25 °C | | 1.10 | 1.20 | V | |
| | | T _j = 150 °C | | 1.00 | 1.10 | V | |
| r _{CE} V _{GE} = 15 V chiplevel | V _{GE} = 15 V | T _j = 25 °C | | 1.78 | 2.2 | mΩ | |
| | chiplevel | T _j = 150 °C | | 2.8 | 3.0 | mΩ | |
| $V_{GE(th)}$ | $V_{GE} = V_{CE}$, $I_C = 18 \text{ mA}$ | | 5.2 | 5.8 | 6.4 | V | |
| I _{CES} | $V_{GE} = 0 \text{ V}, V_{CE} = 17$ | 00 V, T _j = 25 °C | | | 5 | mA | |
| C _{ies} | V _{CE} = 25 V V _{GE} = 0 V | f = 1 MHz | | 36.0 | | nF | |
| Coes | | f = 1 MHz | | 1.50 | | nF | |
| C _{res} | | f = 1 MHz | | 1.14 | | nF | |
| Q_{G} | V _{GE} = - 8 V+ 15 V | | | 3600 | | nC | |
| R _{Gint} | T _j = 25 °C | | | 1.7 | | Ω | |
| t _{d(on)} | $\begin{array}{c} V_{CC} = 900 \text{ V} \\ I_{C} = 450 \text{ A} \\ V_{GE} = +15/-15 \text{ V} \\ R_{G \text{ on}} = 2.7 \Omega \\ R_{G \text{ off}} = 2.7 \Omega \\ \text{di/dt}_{\text{on}} = 4300 \text{ A/}\mu\text{s} \\ \text{di/dt}_{\text{off}} = 2200 \text{ A/}\mu\text{s} \\ \text{dv/dt} = 3200 \text{ V/}\mu\text{s} \\ L_{s} = 21 \text{ nH} \end{array}$ | T _j = 150 °C | | 270 | | ns | |
| t _r | | T _j = 150 °C | | 90 | | ns | |
| Eon | | T _j = 150 °C | | 153 | | mJ | |
| t _{d(off)} | | T _j = 150 °C | | 815 | | ns | |
| t _f | | T _j = 150 °C | | 200 | | ns | |
| E _{off} | | T _j = 150 °C | | 150 | | mJ | |
| R _{th(j-c)} | per IGBT | | | | 0.06 | K/W | |
| R _{th(c-s)} | per IGBT (λ _{grease} =0.81 W/(m*K)) | | | 0.029 | | K/W | |
| R _{th(c-s)} | per IGBT, pre-appli material | | 0.02 | | K/W | | |





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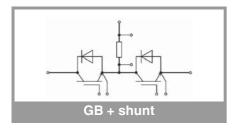
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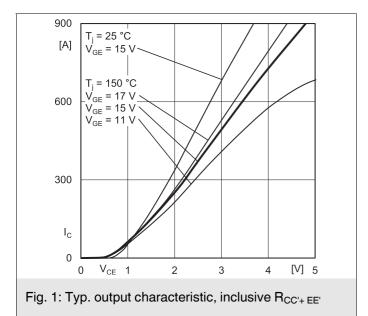
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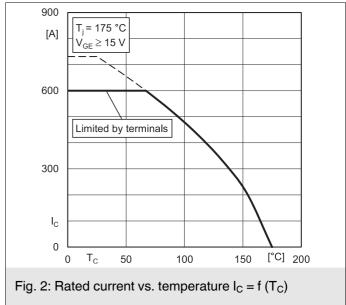
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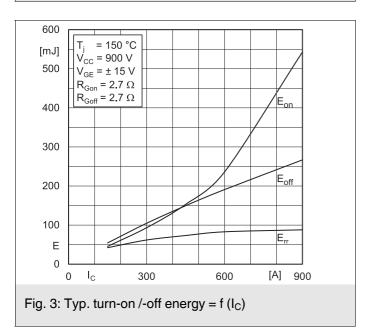
| Characte | eristics | | | | | | | |
|-----------------------|---|-------------------------|------|-------------|------|------|--|--|
| Symbol | Conditions | | min. | typ. | max. | Unit | | |
| Inverse diode | | | | | | | | |
| $V_F = V_{EC}$ | I _F = 450 A | T _j = 25 °C | | 1.98 | 2.37 | V | | |
| | V _{GE} = 0 V chiplevel | T _j = 150 °C | | 2.11 | 2.52 | V | | |
| V _{F0} | chiplevel | T _j = 25 °C | | 1.32 | 1.56 | V | | |
| | | T _j = 150 °C | | 1.08 | 1.22 | V | | |
| r _F | chiplevel | T _j = 25 °C | | 1.46 | 1.80 | mΩ | | |
| | · | T _j = 150 °C | | 2.3 | 2.9 | mΩ | | |
| I _{RRM} | I _F = 450 A | T _j = 150 °C | | 350 | | Α | | |
| Q _{rr} | di/dt _{off} = 4850 A/μs V _{GE} = -15 V | T _j = 150 °C | | 130 | | μC | | |
| E _{rr} | $V_{CC} = 900 \text{ V}$ | T _j = 150 °C | | 73 | | mJ | | |
| R _{th(j-c)} | per diode | | | | 0.1 | K/W | | |
| R _{th(c-s)} | per diode (λ _{grease} =0 | .81 W/(m*K)) | | 0.048 | | K/W | | |
| R _{th(c-s)} | per diode, pre-applied phase change material | | | 0.038 | | K/W | | |
| Module | • | | | | | | | |
| L _{CE} | | | | 20 | | nΗ | | |
| R _{CC'+EE'} | measured per | T _C = 25 °C | | 0.95 | | mΩ | | |
| | switch, shunt excluded | T _C = 125 °C | | 1.25 | | mΩ | | |
| R _{th(c-s)1} | calculated without thermal coupling | | | 0.009 | | K/W | | |
| R _{th(c-s)2} | including thermal coupling, Ts underneath module (λ _{grease} =0.81 W/ (m*K)) | | | 0.014 | | K/W | | |
| R _{th(c-s)2} | including thermal coupling, Ts underneath module, pre-applied phase change material | | | 0.010 | | K/W | | |
| Ms | to heat sink (M5) | to heat sink (M5) | | | 6 | Nm | | |
| M _t | | to terminals (M6) | 3 | | 6 | Nm | | |
| | | | | | | Nm | | |
| W | | | | | 350 | g | | |
| Temperat | ture Sensor | | | | | | | |
| R ₁₀₀ | T_c =100°C (R_{25} =5 k Ω) | | | 493 ± 5% | | Ω | | |
| B _{100/125} | $R_{(T)} = R_{100} exp[B_{100/125}(1/T-1/T_{100})]; T[K];$ | | | 3550 ±2% | | K | | |

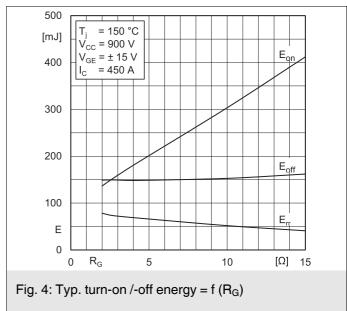
| Characteristics | | | | | | | |
|----------------------|--------------------------------------|------|------|------|-------|--|--|
| Symbol | Conditions | min. | typ. | max. | Unit | | |
| Shunt | | | | | | | |
| R _{Shunt} | Tolerance = ± 1 %, $T_c = 20$ °C | | 0.50 | | mΩ | | |
| α | | | | 50 | ppm/K | | |
| T _{Shunt} | | | | 170 | °C | | |
| R _{th(r-c)} | | | | 3 | K/W | | |
| P _{Shunt} | T _c = 80 °C | | | 30 | W | | |

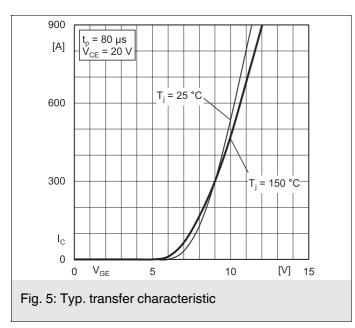


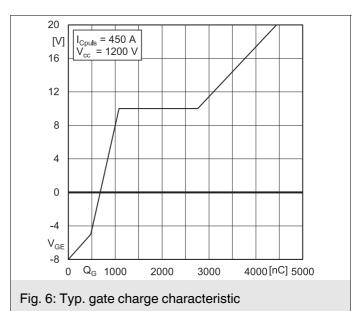


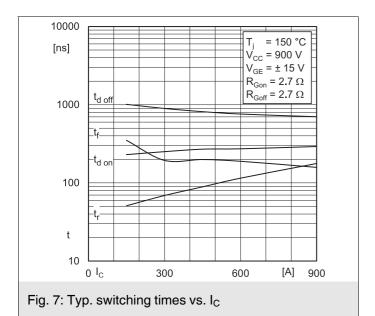


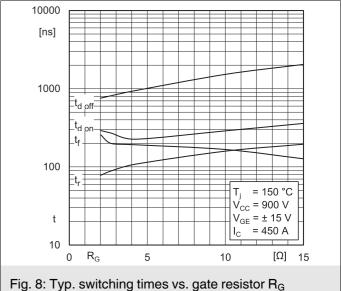












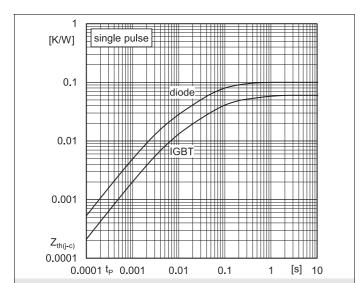
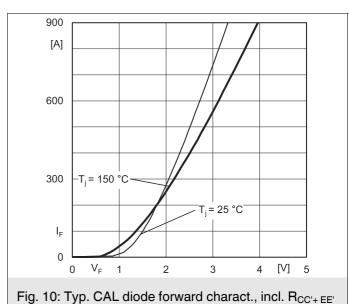
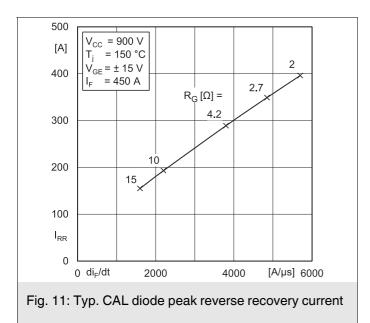
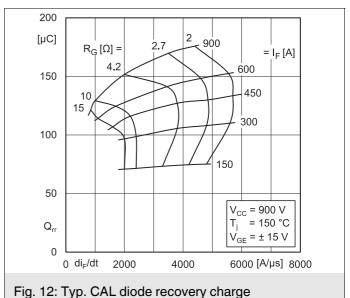
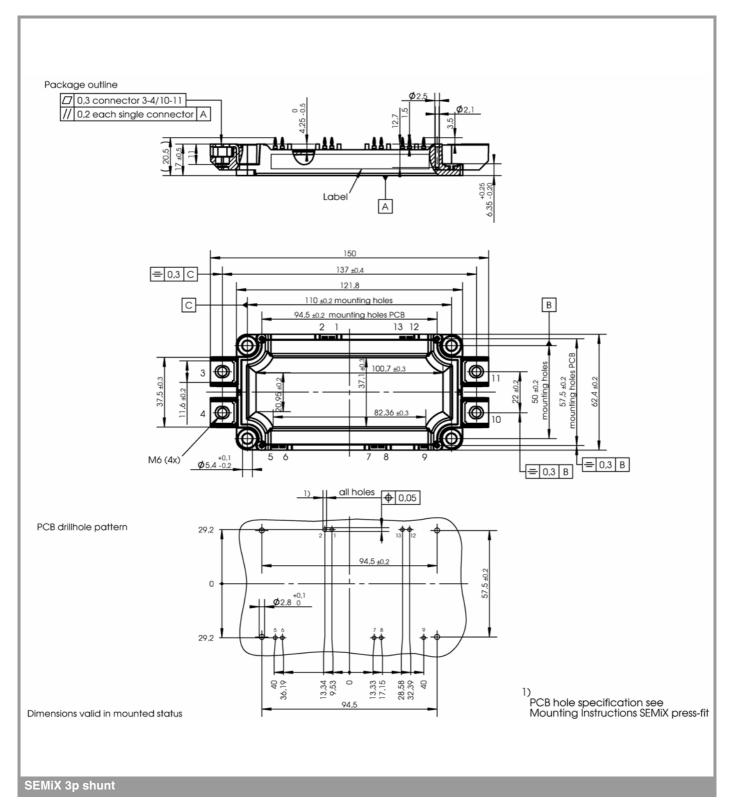


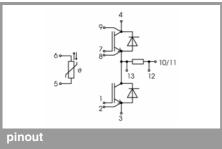
Fig. 9: Transient thermal impedance











This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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