

SEMITOP®E1

Sixpack Open Emitter

SK25GD12T7ETE1

Features*

- Optimized design for superior thermal performance
- Low inductive design
- Press-Fit contact technology
- 1200V Generation 7 IGBT (T7)
- Robust and soft switching CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

Typical Applications

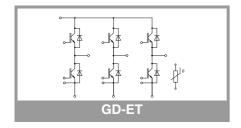
- · Motor drives
- Servo drives
- · Air conditioning
- · Auxiliary Inverters
- UPS

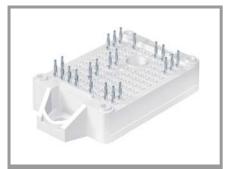
Remarks

- Recommended $T_{j,op} = -40 \dots +150 \,^{\circ}C$
- T_{j,op} > 150 °C during overload (details on AN19-002)

Absolute	Maximum Ratings	S		
Symbol	Conditions		Values	Unit
Inverter -	IGBT			•
V _{CES}	T _j = 25 °C		1200	V
Ic	λ_{paste} =0.8 W/(mK) T _j = 175 °C	T _s = 70 °C	33	Α
		T _s = 100 °C	27	Α
I _C	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	38	Α
	T _j = 175 °C	T _s = 100 °C	31	Α
I _{Cnom}			25	Α
I _{CRM}			50	Α
V _{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 175 °C	7	μѕ
Tj			-40 175	°C
Inverse -	Diode			
V_{RRM}	T _j = 25 °C		1200	V
I _F	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	24	Α
	T _j = 175 °C	T _s = 100 °C	20	Α
	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	28	Α
	T _j = 175 °C	T _s = 100 °C	23	Α
I _{FRM}			50	Α
I _{FSM}	$t_p = 10 \text{ ms, sin } 180^{\circ}, T_j = 150 ^{\circ}\text{C}$		100	Α
Tj			-40 175	°C
Module				
I _{t(RMS)}	, ΔT _{terminal} at PCB joint = 30 K, per pin		30	Α
T _{stg}	module without TIN	Л	-40 125	°C
V _{isol}	AC, sinusoidal, t =	1 min	2500	V

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
Inverter -	IGBT						
V _{CE(sat)}	I _C = 25 A	T _j = 25 °C		1.60	1.75	V	
V _{GE} = 15 V	T _j = 150 °C		1.78	1.93	V		
	chiplevel	T _j = 175 °C		1.82	1.97	V	
V_{CE0}		T _j = 25 °C		1.00	1.05	V	
	chiplevel	T _j = 150 °C		0.80	0.85	V	
		T _j = 175 °C		0.75	0.80	V	
r _{CE}	V 45.V	T _j = 25 °C		24	28	mΩ	
	V _{GE} = 15 V chiplevel	T _j = 150 °C		39	43	mΩ	
		T _j = 175 °C		43	47	mΩ	
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 0.53 \text{ mA}$		5.15	5.8	6.45	V	
I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _j = 25 °C				1	mA	
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		4.80		nF	
C _{oes}		f = 1 MHz		0.06		nF	
C _{res}		f = 1 MHz		0.02		nF	
Q_{G}	V _{GE} = -15 V +15 V			354		nC	
R _{Gint}	T _j = 25 °C			0		Ω	





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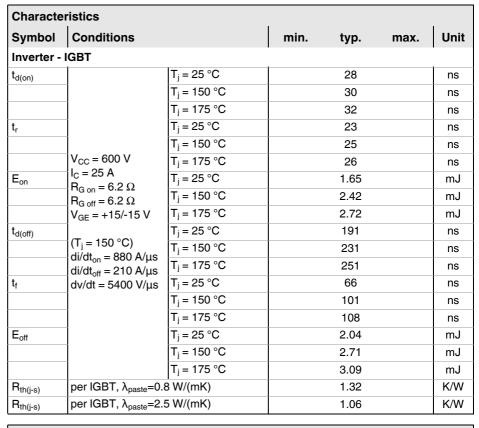
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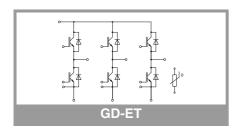
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- T_{j,op} > 150 °C during overload (details on AN19-002)



Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
Inverse -	Diode					•
$V_F = V_{EC}$	I _F = 25 A	T _j = 25 °C		2.41	2.74	V
		T _j = 150 °C		2.45	2.79	V
	chiplevel	T _j = 175 °C		2.30	2.62	V
V_{F0}		T _j = 25 °C		1.30	1.50	V
	chiplevel	T _j = 150 °C		0.90	1.10	V
		T _j = 175 °C		0.82	0.98	V
r _F	chiplevel	T _j = 25 °C		44	50	mΩ
		T _j = 150 °C		62	68	mΩ
		T _j = 175 °C		59	66	mΩ
I _{RRM}		T _j = 25 °C		20		Α
		T _j = 150 °C		28		Α
l-	I _F = 25 A	T _j = 175 °C		30		Α
Q _{rr}	$V_{GE} = +15/-15 \text{ V}$ $V_{CC} = 600 \text{ V}$	T _j = 25 °C		1.41		μC
		T _j = 150 °C		3.71		μC
		T _j = 175 °C		4.19		μC
E _{rr}		T _j = 25 °C		0.51		mJ
		T _j = 150 °C		1.61		mJ
		T _j = 175 °C		2.46		mJ
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			1.66		K/W
R _{th(j-s)}	per Diode, λ _{paste} =2.5 W/(mK)			1.29		K/W
Module	•					
L _{CE}				30		nΗ
Ms	to heatsink		1.6		2.3	Nm
w				25		g





Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Temperature Sensor							
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)	493 ± 5%		Ω			
B _{25/85}	$R_{(T)}=R_{25}*exp[B_{25/85}*(1/T-1/298)], T[K]$	3420		K			

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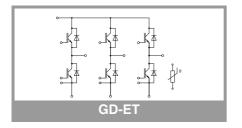
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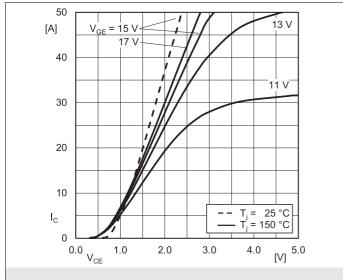


Fig. 1: Typ. IGBT output characteristic, incl. R_{CC+ EE}

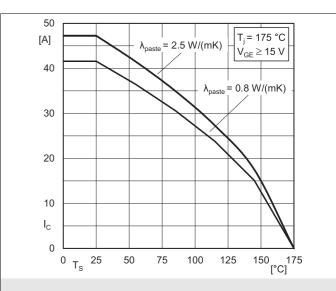


Fig. 2: IGBT rated current vs. temperature I_c=f(T_s)

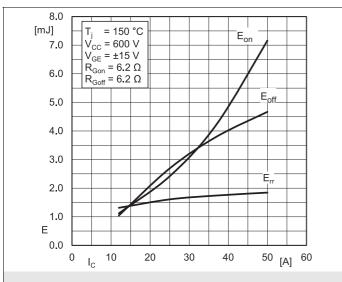


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

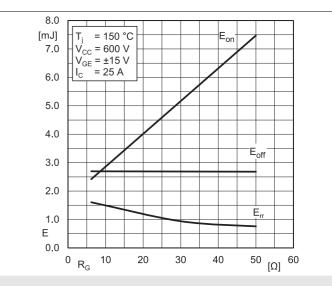


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

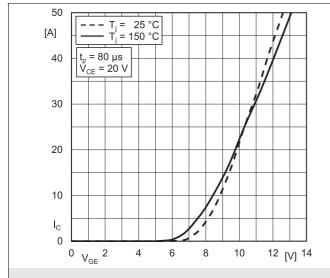


Fig. 5: Typ. IGBT transfer characteristic

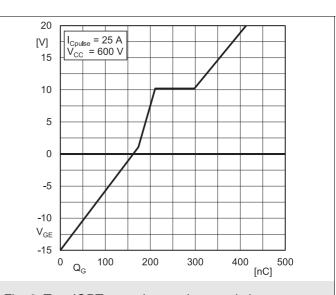
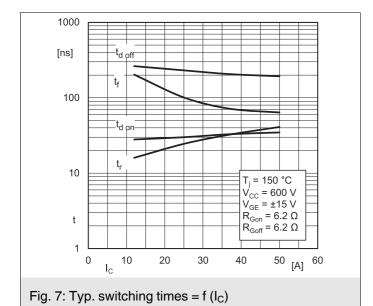
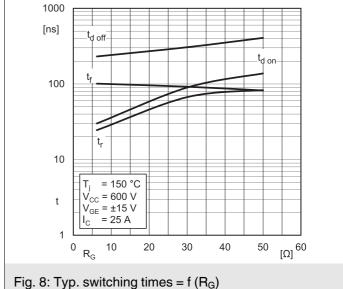
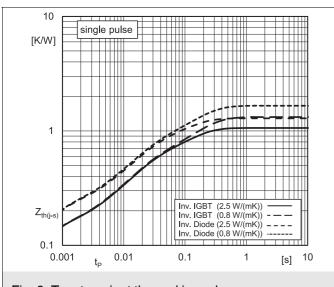
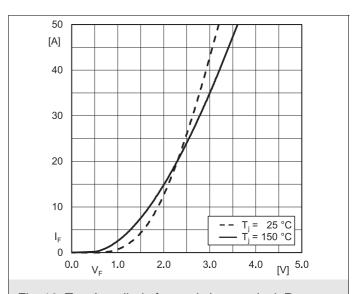


Fig. 6: Typ. IGBT gate charge characteristic









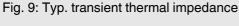
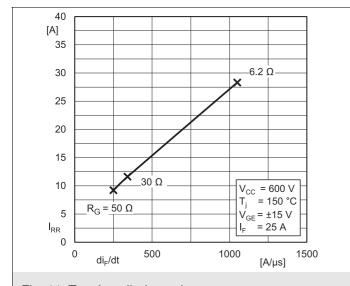


Fig. 10: Typ. Inv. diode forward charact., incl. $R_{CC'+\; EE'}$



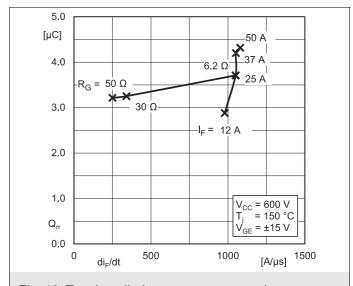
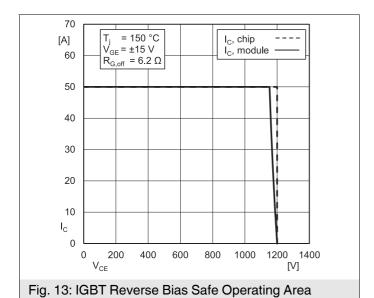
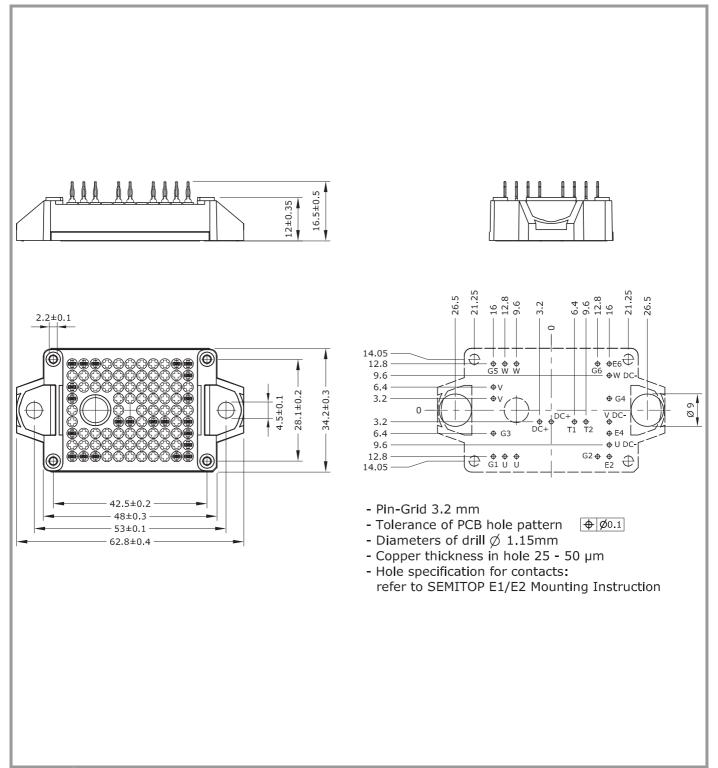


Fig. 11: Typ. Inv. diode peak reverse recovery current

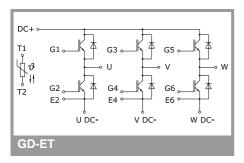
Fig. 12: Typ. Inv. diode reverse recovery charge

(RBSOA)





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This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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