



SEMITOP®E1

Sixpack Open Emitter

SK35GD12T7ETE1

Features*

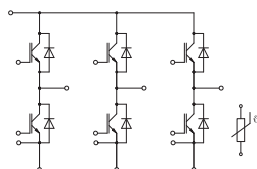
- Optimized design for superior thermal performance
- Low inductive design
- Press-Fit contact technology
- 1200V Generation 7 IGBT (T7)
- Robust and soft switching CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

Typical Applications

- Motor drives
- Servo drives
- Air conditioning
- Auxiliary Inverters
- UPS

Remarks

- Recommended $T_{j,op} = -40 \dots +150 \text{ °C}$
- $T_{j,op} > 150 \text{ °C}$ during overload (details on AN19-002)



GD-ET

Absolute Maximum Ratings				
Symbol	Conditions		Values	Unit
Inverter - IGBT				
V_{CES}	$T_j = 25 \text{ °C}$		1200	V
I_C	$\lambda_{paste}=0.8 \text{ W/(mK)}$	$T_s = 70 \text{ °C}$	41	A
		$T_j = 175 \text{ °C}$	34	A
I_C	$\lambda_{paste}=2.5 \text{ W/(mK)}$	$T_s = 70 \text{ °C}$	50	A
		$T_j = 175 \text{ °C}$	41	A
I_{Cnom}			35	A
I_{CRM}			70	A
V_{GES}			-20 ... 20	V
t_{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \leq 15 \text{ V}$ $V_{CES} \leq 1200 \text{ V}$	$T_j = 175 \text{ °C}$	7	μs
T_j			-40 ... 175	$^{\circ}\text{C}$
Inverse - Diode				
V_{RRM}	$T_j = 25 \text{ °C}$		1200	V
I_F	$\lambda_{paste}=0.8 \text{ W/(mK)}$	$T_s = 70 \text{ °C}$	33	A
		$T_j = 175 \text{ °C}$	27	A
I_F	$\lambda_{paste}=2.5 \text{ W/(mK)}$	$T_s = 70 \text{ °C}$	39	A
		$T_j = 175 \text{ °C}$	32	A
I_{FRM}			70	A
I_{FSM}	$t_p = 10 \text{ ms, sin } 180^{\circ}, T_j = 150 \text{ °C}$		170	A
T_j			-40 ... 175	$^{\circ}\text{C}$
Module				
$I_{t(RMS)}$, $\Delta T_{terminal}$ at PCB joint = 30 K, per pin		30	A
T_{stg}	module without TIM		-40 ... 125	$^{\circ}\text{C}$
V_{isol}	AC, sinusoidal, $t = 1 \text{ min}$		2500	V

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverter - IGBT						
$V_{CE(sat)}$	$I_C = 35 \text{ A}$ $V_{GE} = 15 \text{ V}$ chipelevel	$T_j = 25 \text{ °C}$	1.60	1.75		V
		$T_j = 150 \text{ °C}$	1.78	1.93		V
		$T_j = 175 \text{ °C}$	1.82	1.97		V
V_{CE0}	chipelevel	$T_j = 25 \text{ °C}$	1.00	1.05		V
		$T_j = 150 \text{ °C}$	0.80	0.85		V
		$T_j = 175 \text{ °C}$	0.75	0.80		V
r_{CE}	$V_{GE} = 15 \text{ V}$ chipelevel	$T_j = 25 \text{ °C}$	17	20		$\text{m}\Omega$
		$T_j = 150 \text{ °C}$	28	31		$\text{m}\Omega$
		$T_j = 175 \text{ °C}$	31	33		$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 0.75 \text{ mA}$		5.15	5.8	6.45	V
I_{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_j = 25 \text{ °C}$				1	mA
C_{ies}				6.60		nF
C_{oes}	$V_{CE} = 25 \text{ V}$ $V_{GE} = 0 \text{ V}$	$f = 1 \text{ MHz}$		0.09		nF
C_{res}		$f = 1 \text{ MHz}$		0.02		nF
Q_G	$V_{GE} = -15 \text{ V} \dots +15 \text{ V}$			487		nC
R_{Gint}	$T_j = 25 \text{ °C}$			0		Ω



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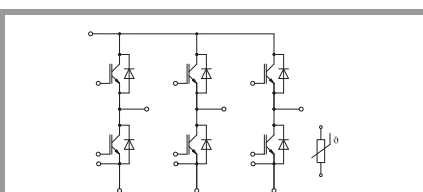
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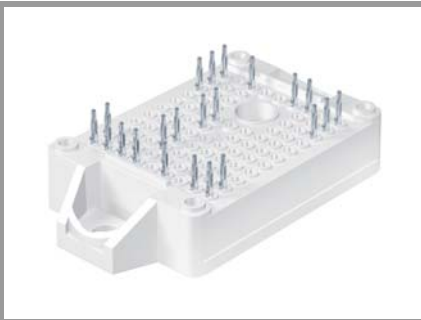


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Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
Inverter - IGBT					
$t_{d(on)}$		$T_j = 25 \text{ °C}$	43		ns
		$T_j = 150 \text{ °C}$	45		ns
		$T_j = 175 \text{ °C}$	46		ns
t_r		$T_j = 25 \text{ °C}$	30		ns
		$T_j = 150 \text{ °C}$	35		ns
		$T_j = 175 \text{ °C}$	37		ns
E_{on}	$V_{CC} = 600 \text{ V}$ $I_C = 35 \text{ A}$ $R_{G on} = 5.6 \text{ } \Omega$ $R_{G off} = 5.6 \text{ } \Omega$ $V_{GE} = +15/-15 \text{ V}$	$T_j = 25 \text{ °C}$	2.51		mJ
		$T_j = 150 \text{ °C}$	3.52		mJ
		$T_j = 175 \text{ °C}$	3.96		mJ
		$T_j = 25 \text{ °C}$	183		ns
$t_{d(off)}$	$(T_j = 150 \text{ °C})$ $di/dt_{on} = 1160 \text{ A}/\mu\text{s}$ $di/dt_{off} = 620 \text{ A}/\mu\text{s}$ $dv/dt = 4600 \text{ V}/\mu\text{s}$	$T_j = 150 \text{ °C}$	254		ns
		$T_j = 175 \text{ °C}$	274		ns
		$T_j = 25 \text{ °C}$	62		ns
t_f		$T_j = 150 \text{ °C}$	95		ns
		$T_j = 175 \text{ °C}$	102		ns
		$T_j = 25 \text{ °C}$	2.83		mJ
E_{off}		$T_j = 150 \text{ °C}$	3.74		mJ
		$T_j = 175 \text{ °C}$	4.29		mJ
		$T_j = 25 \text{ °C}$	1.17		K/W
$R_{th(j-s)}$	per IGBT, $\lambda_{paste}=0.8 \text{ W}/(\text{mK})$		1.17		K/W
$R_{th(j-s)}$	per IGBT, $\lambda_{paste}=2.5 \text{ W}/(\text{mK})$		0.85		K/W

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
Inverse - Diode					
$V_F = V_{EC}$	$I_F = 35 \text{ A}$	$T_j = 25 \text{ °C}$	2.30	2.62	V
		$T_j = 150 \text{ °C}$	2.29	2.62	V
		chiplevel $T_j = 175 \text{ °C}$	2.14	2.46	V
V_{F0}	chiplevel	$T_j = 25 \text{ °C}$	1.30	1.50	V
		$T_j = 150 \text{ °C}$	0.90	1.10	V
		$T_j = 175 \text{ °C}$	0.82	0.98	V
r_F	chiplevel	$T_j = 25 \text{ °C}$	29	32	m Ω
		$T_j = 150 \text{ °C}$	40	43	m Ω
		$T_j = 175 \text{ °C}$	38	42	m Ω
I_{RRM}		$T_j = 25 \text{ °C}$	25		A
		$T_j = 150 \text{ °C}$	31		A
		$T_j = 175 \text{ °C}$	37		A
Q_{rr}	$I_F = 35 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$ $V_{CC} = 600 \text{ V}$ $(T_j = 150 \text{ °C})$	$T_j = 25 \text{ °C}$	2.15		μC
		$T_j = 150 \text{ °C}$	4.85		μC
		$T_j = 175 \text{ °C}$	5.48		μC
E_{rr}	$di/dt_{off} = 1030 \text{ A}/\mu\text{s}$	$T_j = 25 \text{ °C}$	1.46		mJ
		$T_j = 150 \text{ °C}$	2.39		mJ
		$T_j = 175 \text{ °C}$	3.65		mJ
$R_{th(j-s)}$	per Diode, $\lambda_{paste}=0.8 \text{ W}/(\text{mK})$		1.34		K/W
$R_{th(j-s)}$	per Diode, $\lambda_{paste}=2.5 \text{ W}/(\text{mK})$		1.01		K/W
Module					
L_{CE}			30		nH
M_s	to heatsink	1.6		2.3	Nm
w			25		g

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Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
Temperature Sensor					
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)		493 ± 5%		Ω
B _{25/85}	R _(T) =R ₂₅ *exp[B _{25/85} *(1/T-1/298)], T[K]		3420		K

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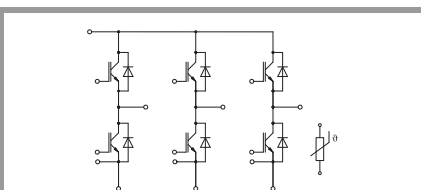
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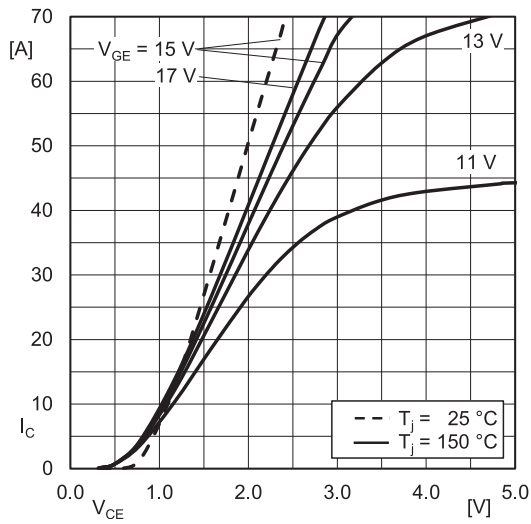


Fig. 1: Typ. IGBT output characteristic, incl. R_{CC+EE}

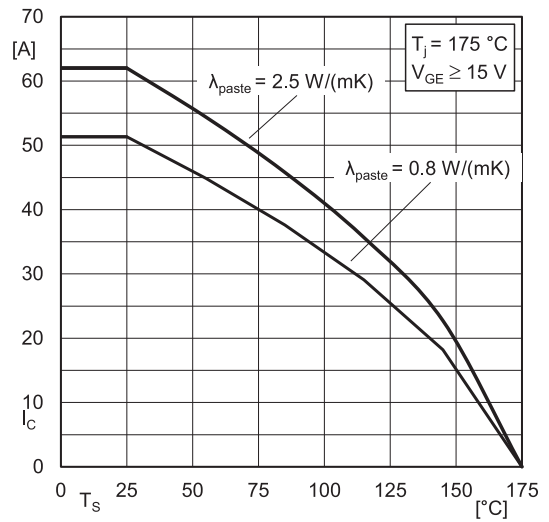


Fig. 2: IGBT rated current vs. temperature $I_c=f(T_s)$

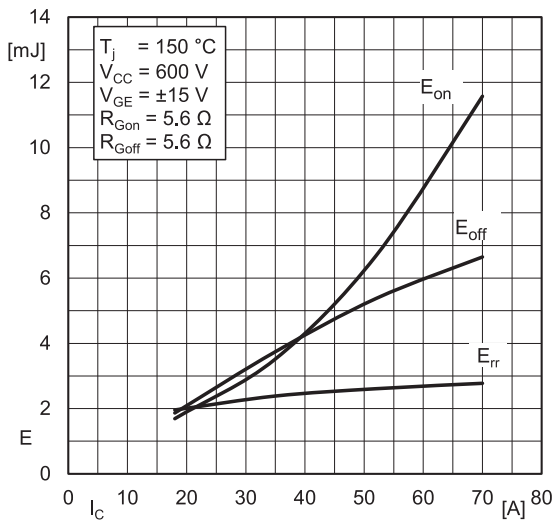


Fig. 3: Typ. turn-on /-off energy = $f(I_c)$

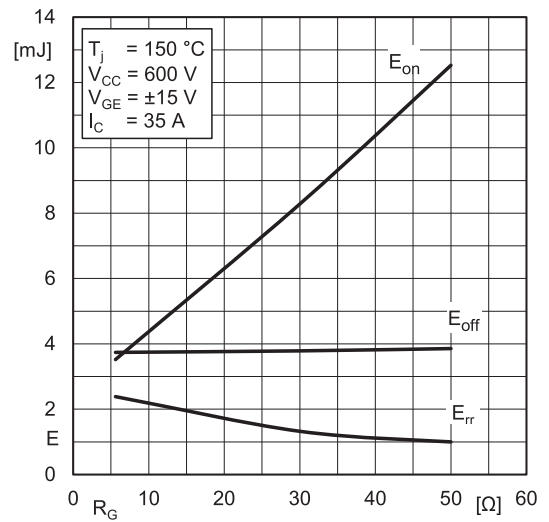


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

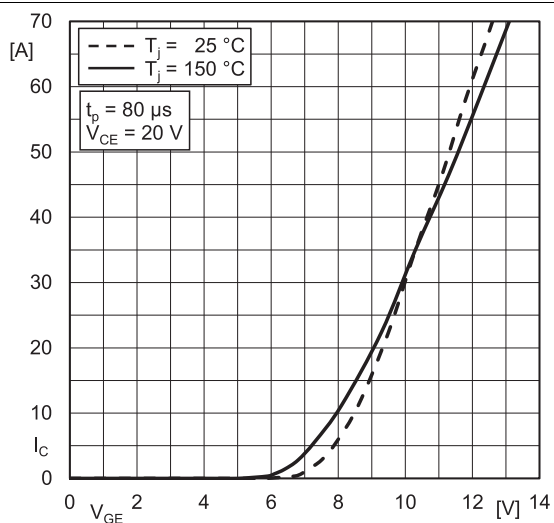


Fig. 5: Typ. IGBT transfer characteristic

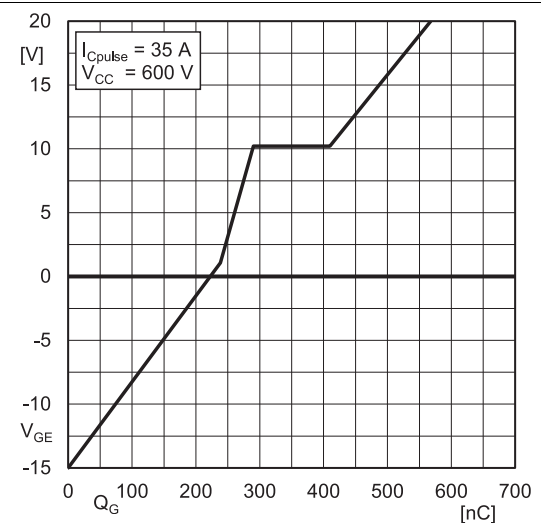


Fig. 6: Typ. IGBT gate charge characteristic

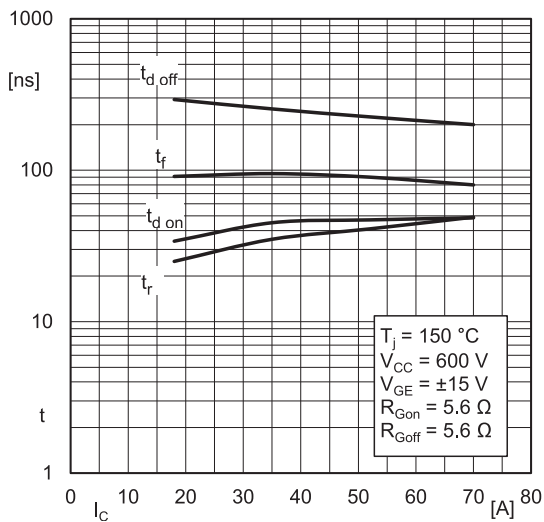


Fig. 7: Typ. switching times = $f(I_C)$

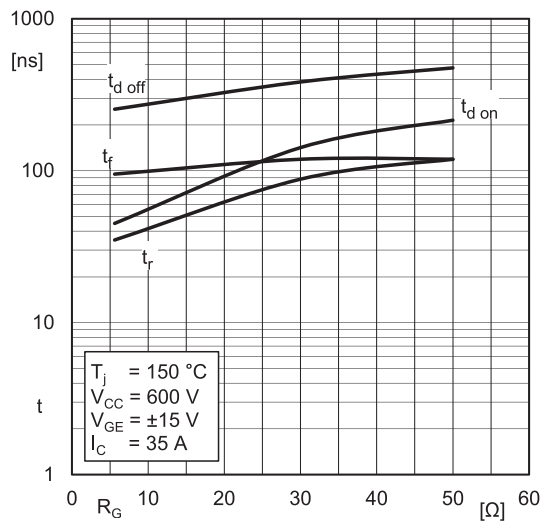


Fig. 8: Typ. switching times = $f(R_G)$

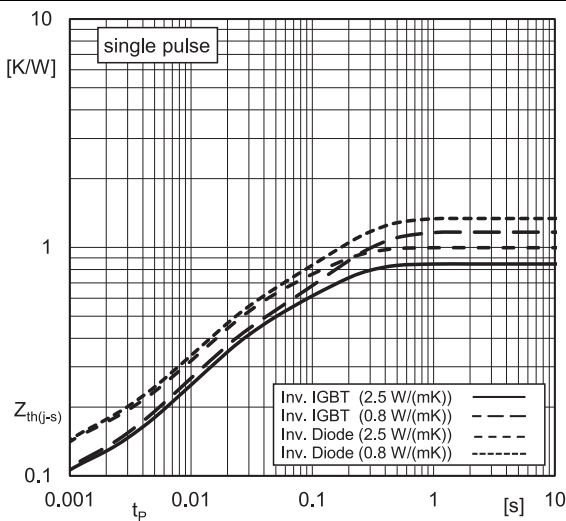


Fig. 9: Typ. transient thermal impedance

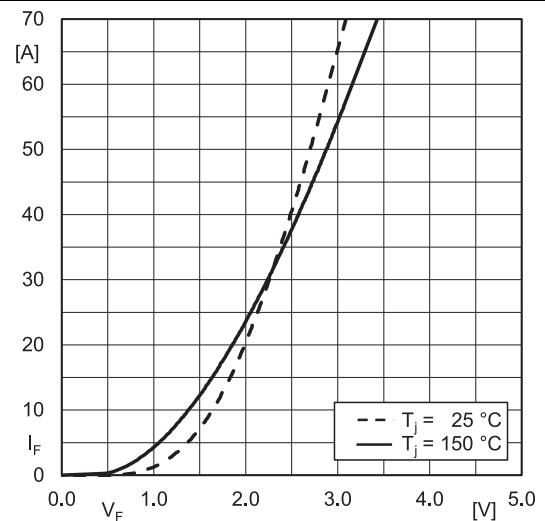


Fig. 10: Typ. Inv. diode forward charact., incl. $R_{CC+EE'}$

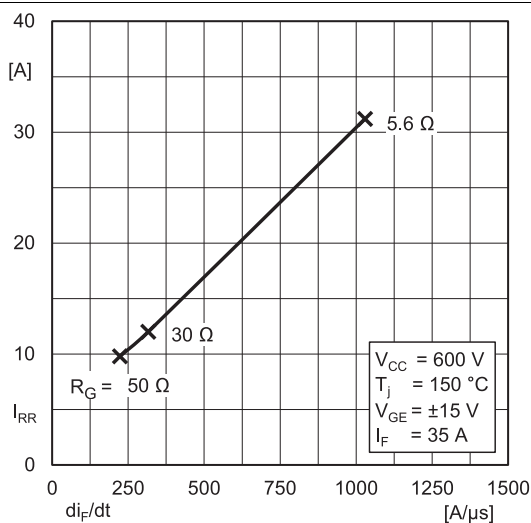


Fig. 11: Typ. Inv. diode peak reverse recovery current

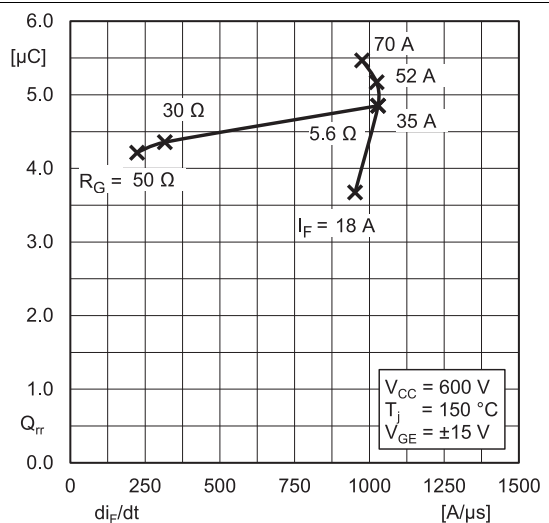
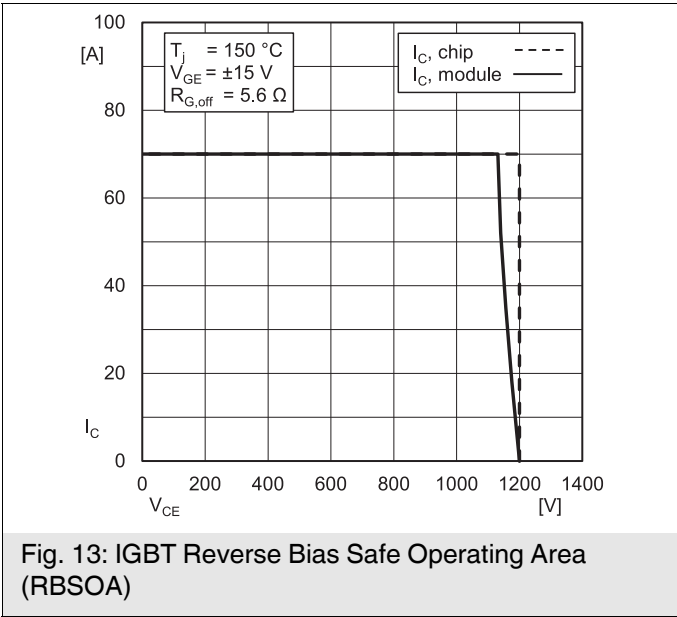
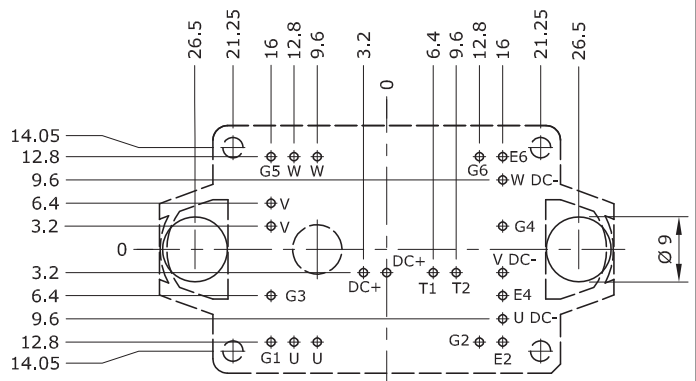
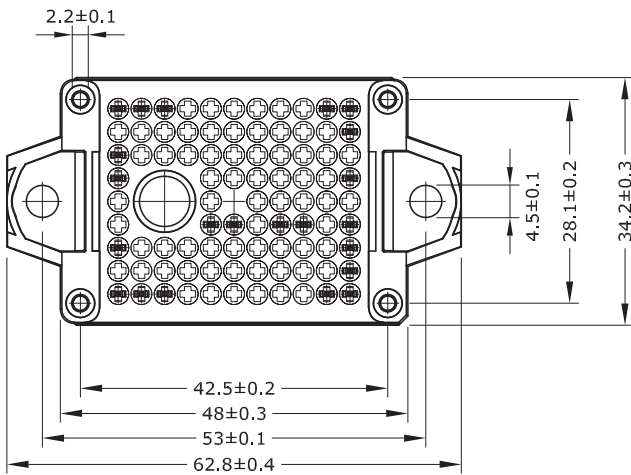
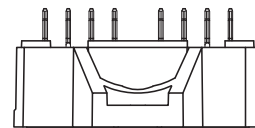
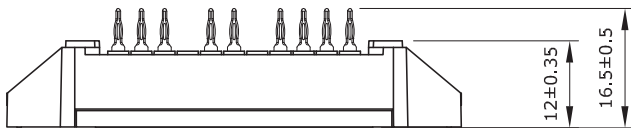


Fig. 12: Typ. Inv. diode reverse recovery charge

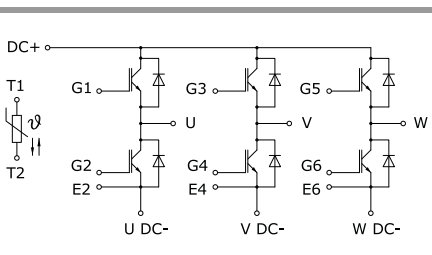


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- Pin-Grid 3.2 mm
- Tolerance of PCB hole pattern $\text{⌀} \pm 0.1$
- Diameters of drill $\text{⌀} 1.15\text{mm}$
- Copper thickness in hole 25 - 50 μm
- Hole specification for contacts:
refer to SEMITOP E1/E2 Mounting Instruction

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This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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