

SEMITOP®E2

IGBT module

SK50GD12T4ETE2

Features*

- · Low inductive design
- · Press-Fit contact technology
- Rugged mounting due to integrated mounting clamps
- Heat transfer and insulation through direct copper bonded aluminium oxide ceramic (DBC)
- Trench4 IGBT technology
- Robust and soft switching CAL4F diode technology
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

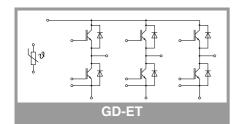
Typical Applications

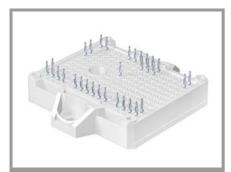
- Motor drives
- · Servo drives
- · Air conditioning
- Auxiliary Inverters
- UPS

Absolute Maximum Ratings						
Symbol	Conditions		Values	Unit		
IGBT 1			•			
V _{CES}	T _j = 25 °C		1200	V		
I _C	λ _{paste} =0.8 W/(mK)	T _s = 25 °C	65	Α		
	T _j = 175 °C	T _s = 70 °C	53	Α		
I _C	λ _{paste} =2.5 W/(mK)	T _s = 25 °C	82	Α		
	T _j = 175 °C	T _s = 70 °C	67	Α		
I _{Cnom}			50	Α		
I _{CRM}	$I_{CRM} = 3 \times I_{Cnom}$		150	Α		
V _{GES}			-20 20	V		
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 150 °C	10	μs		
Tj			-40 175	°C		

Absolute Maximum Ratings						
Symbol	Conditions		Values	Unit		
Diode 1				•		
V_{RRM}	T _j = 25 °C		1200	V		
I _F	λ _{paste} =0.8 W/(mK)	T _s = 25 °C	56	Α		
T _j = 175 °C		T _s = 70 °C	45	Α		
I _F	λ _{paste} =2.5 W/(mK)	T _s = 25 °C	69	Α		
	T _j = 175 °C	T _s = 70 °C	55	Α		
I _{Fnom}		1	50	Α		
I _{FRM}	$I_{FRM} = 2 \times I_{Fnom}$		100	Α		
I _{FSM}	10 ms	T _j = 25 °C	270	Α		
	sin 180°	T _j = 150 °C	270	Α		
T _i		1	-40 175	°C		

Absolute Maximum Ratings						
Symbol Conditions Values			Unit			
Module						
I _{t(RMS)}	ΔT _{terminal} at PCB joint = 30 K, per pin	30	Α			
T _{stg}		-40 125	°C			
V _{isol}	AC, sinusoidal, t = 1 min	2500	V			





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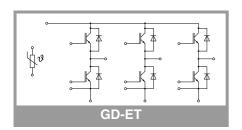
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Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
IGBT 1						•	
V _{CE(sat)}	$I_{\rm C} = 50 {\rm A}$	T _j = 25 °C		1.85	2.10	V	
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.20	2.40	V	
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V	
	Criipievei	T _j = 150 °C		0.70	0.80	V	
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		21	24	mΩ	
	chiplevel	T _j = 150 °C		30	32	mΩ	
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 1.7$	mA	5	5.8	6.5	V	
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	00 V, T _j = 25 °C			1	mA	
C _{ies}		f = 1 MHz		2.77		nF	
Coes	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		0.205		nF	
C _{res}	VGE - O V	f = 1 MHz		0.16		nF	
Q_{G}	V _{GE} = -15V +15V			369		nC	
R _{Gint}	T _j = 25 °C			4.0		Ω	
t _{d(on)}	V _{CC} = 600 V	T _j = 150 °C		129		ns	
t _r	$I_{\rm C} = 50 \text{ A}$	T _j = 150 °C		42		ns	
Eon	$V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 13 \Omega$	T _j = 150 °C		4.8		mJ	
t _{d(off)}		T _j = 150 °C		333		ns	
t _f	di/dt _{on} = 2169 A/μs	T _j = 150 °C		65		ns	
E _{off}	di/dt _{off} = 534 A/μs dv/dt = 4035 V/μs	T _j = 150 °C		5		mJ	
R _{th(j-s)}	per IGBT, λ _{paste} =0.8 W/(mK)			0.77		K/W	
R _{th(j-s)}	per IGBT, λ _{paste} =2.5 W/(mK)			0.52		K/W	

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
Diode 1			•			•	
V _F	I _F = 50 A	T _j = 25 °C		2.22	2.54	V	
	chiplevel	T _j = 150 °C		2.18	2.50	V	
V_{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V	
	Chipievei	T _j = 150 °C		0.90	1.10	V	
r _F	chiplevel	T _j = 25 °C		18	21	mΩ	
		T _j = 150 °C		26	28	mΩ	
I _{RRM}	I _F = 50 A	T _j = 150 °C		70		Α	
Q _{rr}	di/dt _{off} = 2169 A/μs	T _j = 150 °C		7.01		μC	
E _{rr}	$V_{GE} = -15 \text{ V}$ $V_{CC} = 600 \text{ V}$	T _j = 150 °C		2.89		mJ	
R _{th(j-s)}	per Diode, $\lambda_{paste}=0$.	8 W/(mK)		1.06		K/W	
R _{th(j-s)}	per Diode, λ _{paste} =2.	5 W/(mK)		0.76		K/W	





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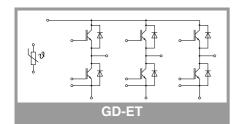
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Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Module						
Ms	to heatsink	1.6		2.3	Nm	
W	weight		35		g	

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Temperati	Temperature Sensor						
R ₁₀₀	T _r = 100 °C		493 ± 5%		Ω		
B _{100/125}	$R_{(T)}=R_{100}exp[B_{100/125}(1/T-1/T_{100})]; T[K];$	3550 ±2%		K			



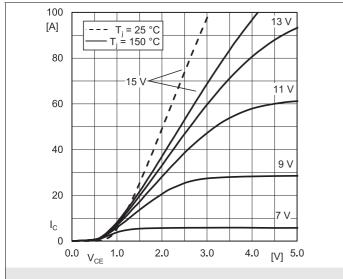


Fig. 1: Typ. IGBT output characteristic, incl. R_{CC'+ EE'}

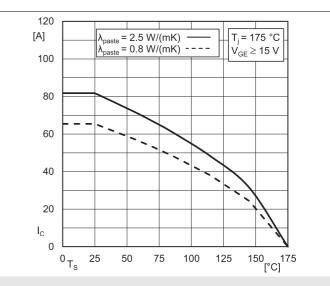


Fig. 2: IGBT rated current vs. temperature I_c=f(T_s)

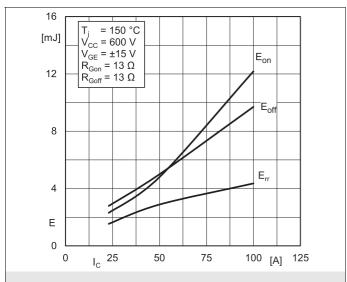


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

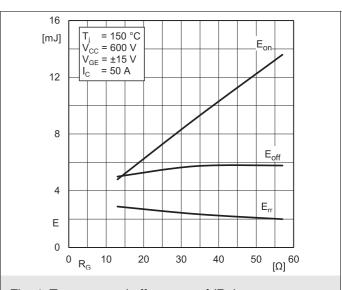


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

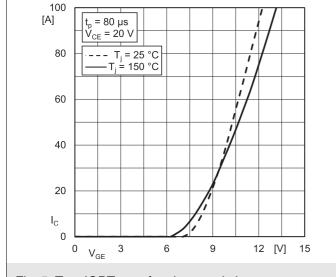


Fig. 5: Typ. IGBT transfer characteristic

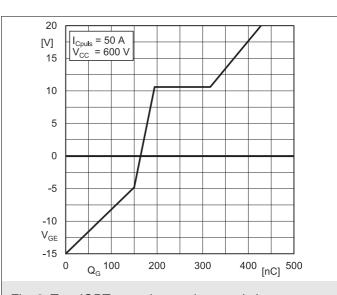


Fig. 6: Typ. IGBT gate charge characteristic

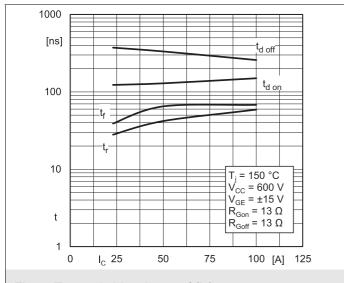


Fig. 7: Typ. switching times = $f(I_C)$

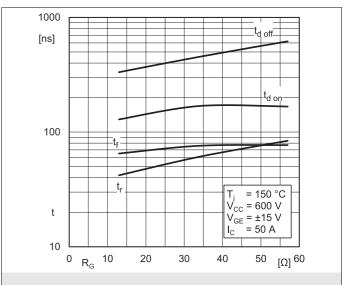


Fig. 8: Typ. switching times = $f(R_G)$

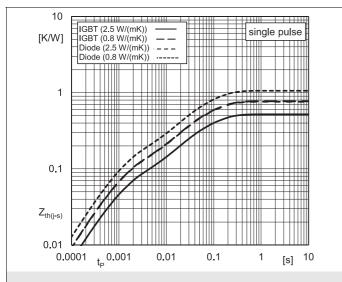


Fig. 9: Typ. transient thermal impedance

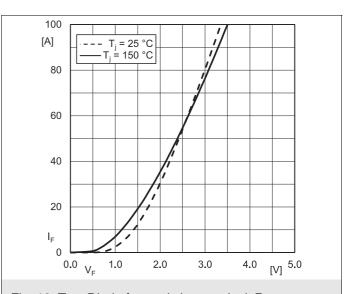


Fig. 10: Typ. Diode forward charact., incl. $R_{CC'+\; EE'}$

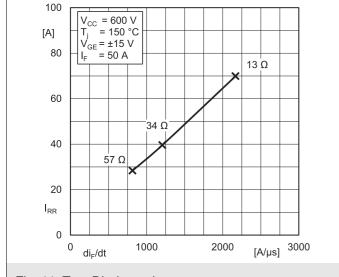


Fig. 11: Typ. Diode peak reverse recovery current

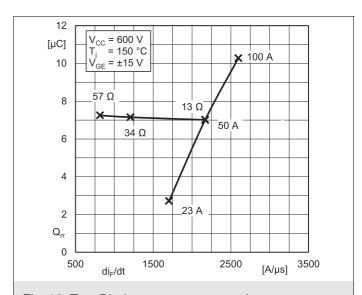


Fig. 12: Typ. Diode reverse recovery charge

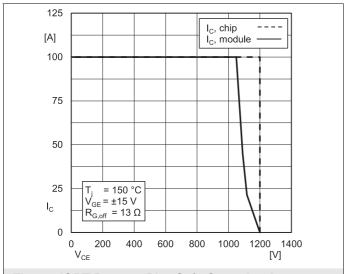
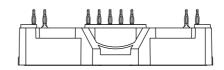
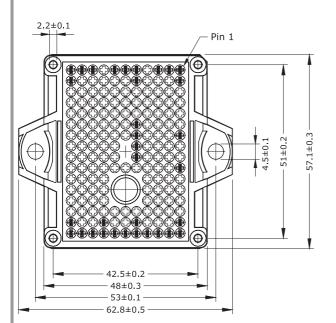


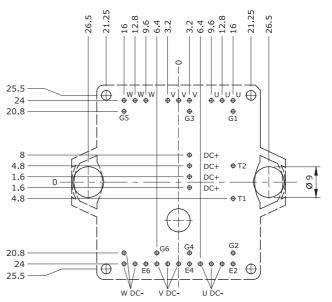
Fig. 13: IGBT Reverse Bias Safe Operating Area (RBSOA)



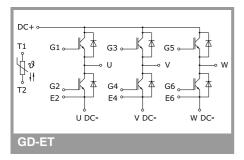




- Pin-Grid 3.2 mm
- Tolerance of PCB hole pattern ⊕ 0.025
- Diameters of drill $ot \emptyset 1.15$ mm
- Copper thickness in hole 25 50 μm
- Hole specification for contacts: refer to SEMITOP E1, E2 mounting instructions



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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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