

SEMIPACK® 2

Thyristor Modules

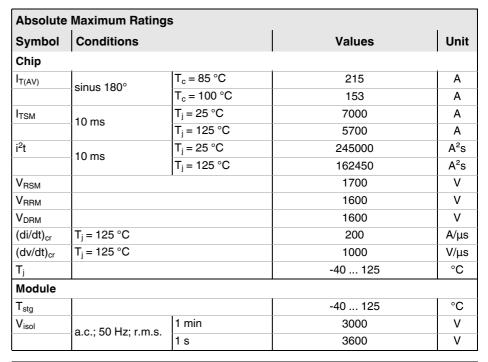
SKKT 215/16 E

Features

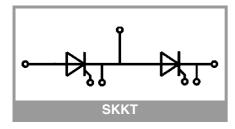
- Heat transfer through aluminium oxide ceramic insulated metal baseplate
- · Hard soldered joints for high reliability
- UL recognized, file no. E63532

Typical Applications*

- DC motor control (e. g. for machine tools)
- AC motor soft starters
- Temperature control (e. g. for ovens, chemical processes)
- Professional light dimming (studios, theaters)



Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Chip						
V _T	$T_j = 25 ^{\circ}\text{C}, I_T = 600 \text{A}$				1.5	V
$V_{T(TO)}$	T _j = 125 °C				0.85	V
r _T	T _j = 125 °C				1.2	mΩ
$I_{DD};I_{RD}$	$T_j = 125$ °C, $V_{DD} = V_{DRM}$; $V_{RD} = V_{RRM}$				60	mA
t _{gd}	$T_j = 25 ^{\circ}C$, $I_G = 1 A$, $di_G/dt = 1 A/\mu s$			1		μs
t _{gr}	$V_{D} = 0.67 * V_{DRM}$			2		μs
tq	T _j = 125 °C			150		μs
I _H	T _j = 25 °C			150	400	mA
IL	$T_j = 25 ^{\circ}\text{C}, R_G = 33 \Omega$			300	1000	mA
V_{GT}	$T_j = 25$ °C, d.c.		2			V
I _{GT}	$T_j = 25$ °C, d.c.		150			mA
V_{GD}	T _j = 125 °C, d.c.				0.25	V
I _{GD}	$T_j = 125 ^{\circ}\text{C}, \text{d.c.}$				10	mA
R _{th(j-c)}	cont.	per chip			0.12	K/W
		per module			0.06	K/W
R _{th(j-c)}	sin. 180°	per chip			0.125	K/W
		per module			0.065	K/W
R _{th(j-c)}	rec. 120°	per chip			0.14	K/W
		per module			0.07	K/W
Module		•				
R _{th(c-s)}	chip			0.04		K/W
	module			0.027		K/W
Ms	to heatsink M5		4.25		5.75	Nm
M _t	to terminals M6		4.25		5.75	Nm
а					5 * 9.81	m/s²
W				165		g



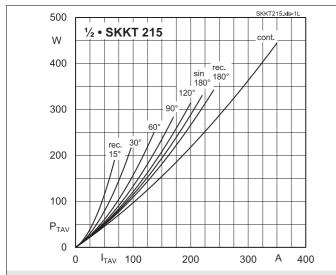


Fig. 1L: Power dissipation per thyristor vs. on-state current

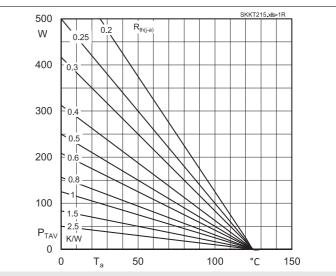


Fig. 1R: Max. power dissipation per chip vs. ambient temperature

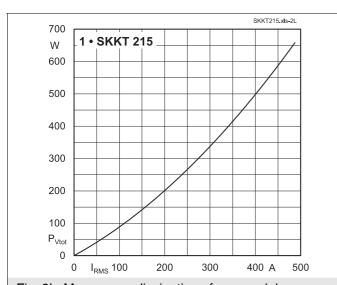


Fig. 2L: Max. power dissipation of one module vs. rms current

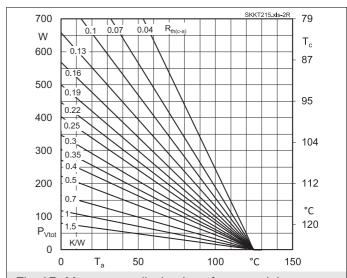


Fig. 2R: Max. power dissipation of one module vs. case temperature

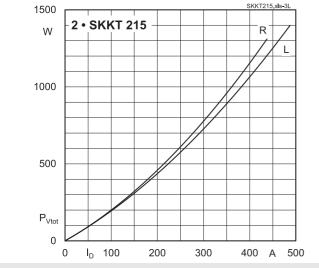


Fig. 3L: Max. power dissipation of two modules vs. direct current

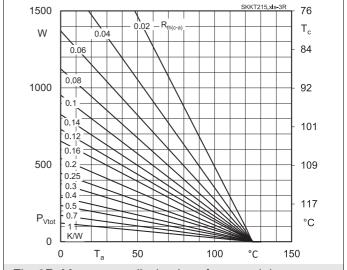


Fig. 3R: Max. power dissipation of two modules vs. case temperature

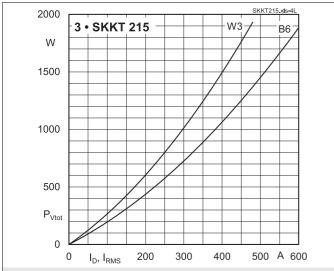


Fig. 4L: Max. power dissipation of three modules vs. direct current

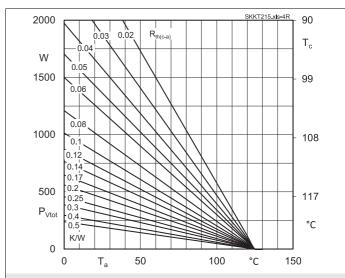


Fig. 4R: Max. power dissipation of three modules vs. case temperature

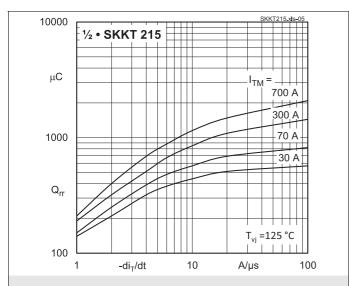


Fig. 5: Recovered charge vs. current decrease

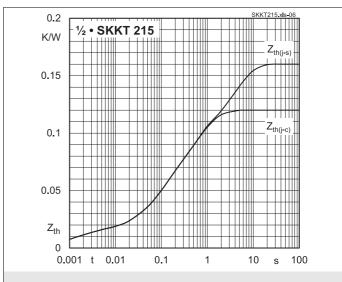


Fig. 6: Transient thermal impedance vs. time

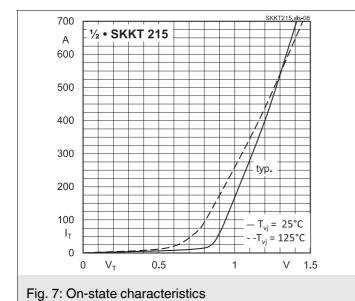
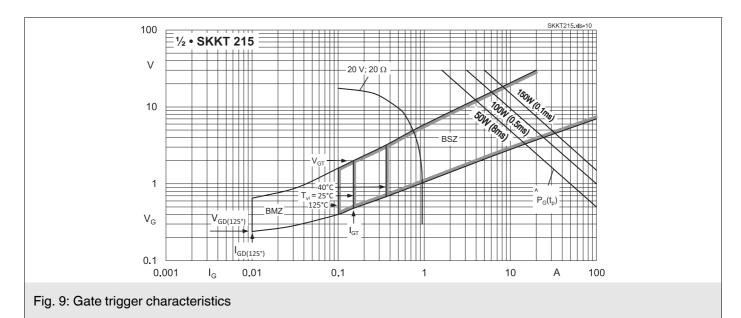
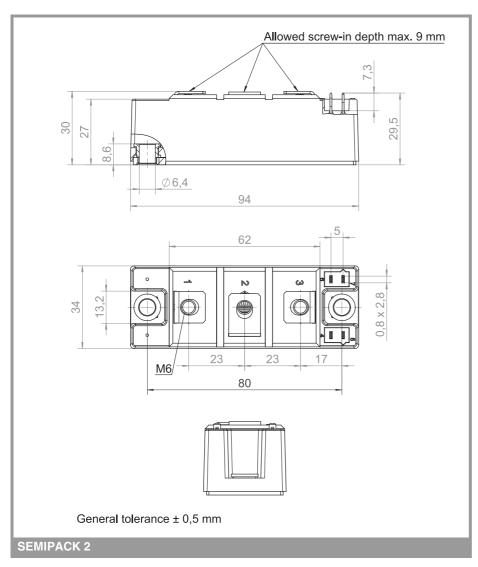
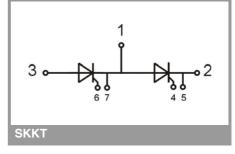


Fig. 8: Surge overload current vs. time







This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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