

SEMITRANS[®] 3

IGBT4 Modules

SKM450GB12E4D1

Features*

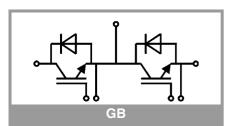
- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- Increased power cycling capability
- With integrated gate resistor
- For higher switching frequencies up to 12kHz
- UL recognized, file no. E63532
 SKM...D1: increased diode performance

Typical Applications

- AC inverter drives
- UPS

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended $T_{op} = -40 \dots +150^{\circ}C$
- Product reliability results valid for T_i = 150°C



Absolute	e Maximum Rating	js		
Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _j = 25 °C		1200	V
lc	T _j = 175 °C	T _c = 25 °C	699	А
		T _c = 80 °C	538	А
I _{Cnom}			450	A
I _{CRM}	$I_{CRM} = 3 \times I_{Cnom}$		1350	А
V _{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 V$ $V_{GE} \le 15 V$ $V_{CES} \le 1200 V$	T _j = 150 °C	10	μs
Tj			-40 175	°C
Inverse d	liode			•
V _{RRM}	T _j = 25 °C		1200	V
I _F	T 175 00	T _c = 25 °C	623	Α
Ij	T _j = 175 °C	T _c = 80 °C	466	А
I _{Fnom}			500	А
I _{FRM}	I _{FRM} = 2xI _{Fnom}		1000	Α
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		2736	Α
Tj			-40 175	°C
Module	•		•	•
I _{t(RMS)}			500	Α
T _{stg}	module without TIM		-40 125	°C
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	V

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
IGBT							
$\begin{array}{c c} V_{CE(sat)} & I_C = 450 \text{ A} \\ V_{GE} = 15 \text{ V} \\ chiplevel \end{array}$	•	T _j = 25 °C		1.84	2.07	V	
	T _j = 150 °C		2.23	2.42	V		
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V	
		T _j = 150 °C		0.70	0.80	V	
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		2.3	2.6	mΩ	
	chiplevel	T _j = 150 °C		3.4	3.6	mΩ	
V _{GE(th)}	$V_{GE} = V_{CE}, I_{C} = 16.4 \text{ mA}$		5	5.8	6.5	V	
I _{CES}	$V_{GE} = 0 V, V_{CE} = 12$	00 V, T _j = 25 °C			5	mA	
Cies	$V_{or} = 25 V$	f = 1 MHz		27.2		nF	
Coes		f = 1 MHz		1.76		nF	
C _{res}		f = 1 MHz		1.50		nF	
Q_{G}	V _{GE} = - 8 V+ 15 V			2500		nC	
R _{Gint}	T _j = 25 °C			1.9		Ω	
t _{d(on)}	$V_{GE} = +15/-15 V$ $R_{G on} = 1 \Omega$	T _j = 150 °C		253		ns	
t _r		T _j = 150 °C		59		ns	
Eon		T _j = 150 °C		28		mJ	
t _{d(off)}		T _j = 150 °C		505		ns	
t _f		T _j = 150 °C		112		ns	
E _{off}		T _j = 150 °C		58		mJ	
R _{th(j-c)}	per IGBT				0.062	K/W	
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.028		K/W	
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.017		K/W	



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- performance

Typical Applications

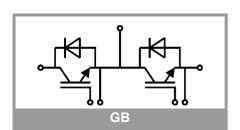
AC inverter drives

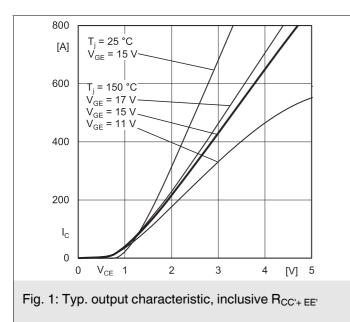
• UPS

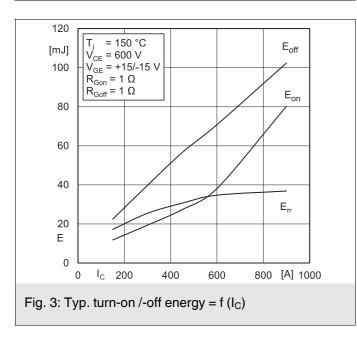
Remarks

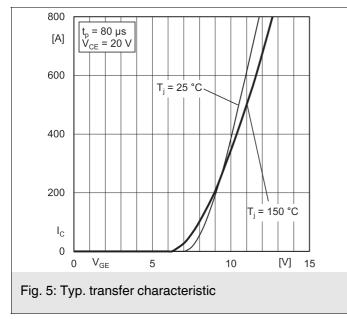
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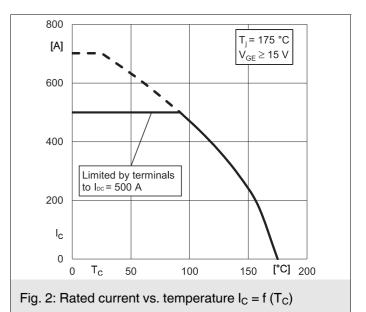
Symbol	Conditions	min.	typ.	max.	Unit	
Inverse d	iode					
$V_F = V_{EC}$	I _F = 450 A V _{GE} = 0 V chiplevel	T _j = 25 °C		2.04	2.35	V
		T _j = 150 °C		1.94	2.23	V
V _{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90	1.10	V
r _F	chiplevel	T _j = 25 °C		1.64	1.88	mΩ
		T _j = 150 °C		2.3	2.5	mΩ
I _{RRM}		T _j = 150 °C		504		Α
Q _{rr}		T _j = 150 °C		75		μC
E _{rr}		T _j = 150 °C		31		mJ
R _{th(j-c)}	per diode				0.095	K/W
R _{th(c-s)}	per diode (λ _{grease} =0.81 W/(m*K))			0.037		K/W
R _{th(c-s)}	per diode, pre-applied phase change material			0.03		K/W
Module						•
L _{CE}				15		nH
R _{CC'+EE'}	measured per	T _C = 25 °C		0.55		mΩ
	switch	T _C = 125 °C		0.85		mΩ
R _{th(c-s)1}	calculated without thermal coupling			0.008		K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module $(\lambda_{grease}=0.81 \text{ W}/(\text{m}^{*}\text{K}))$			0.013		K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.009		K/W
Ms	to heat sink M6		3		5	Nm
Mt		to terminals M6	2.5		5	Nm
						Nm
w					325	g

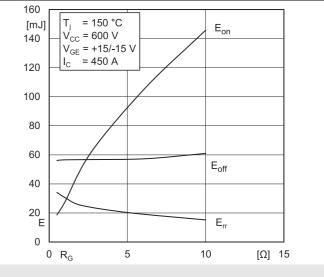


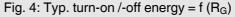


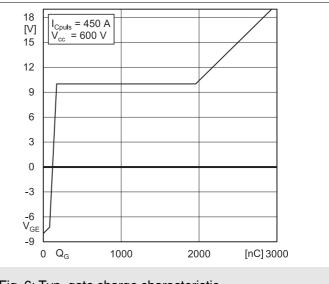




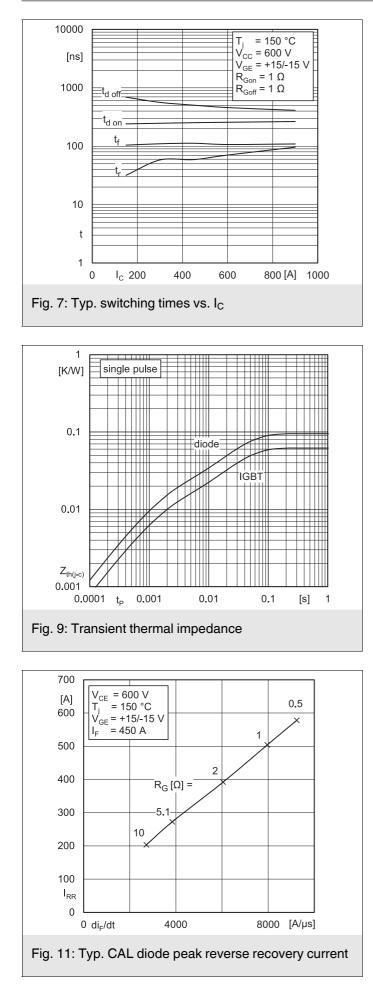












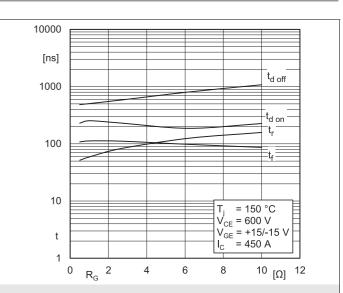
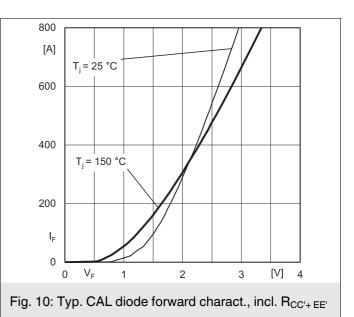
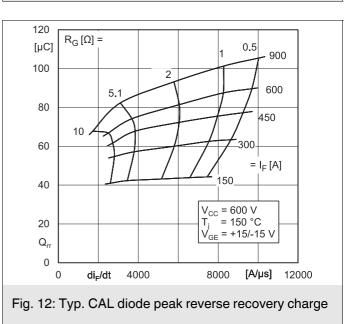
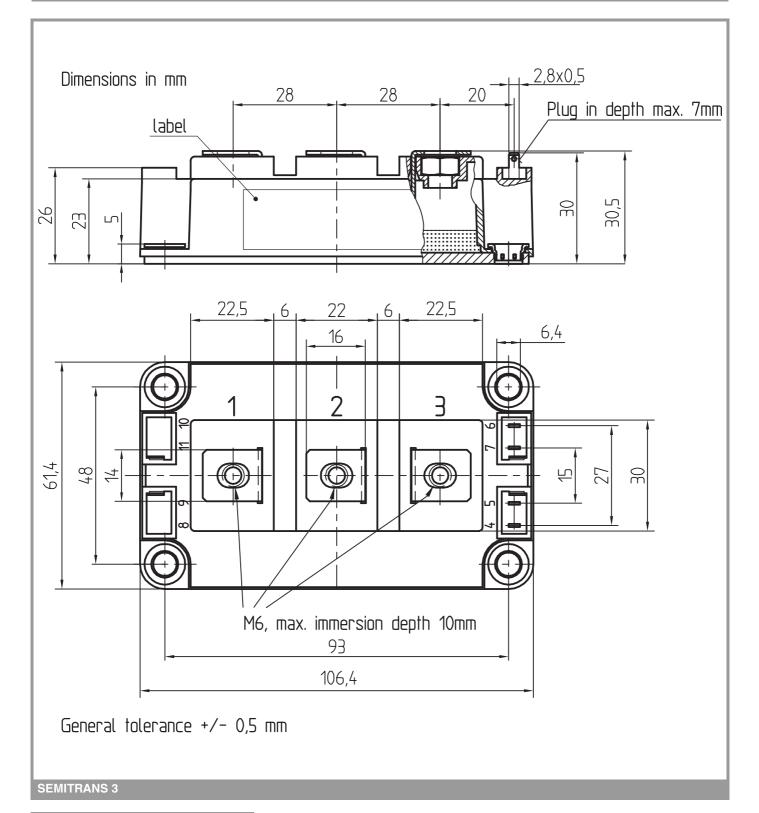
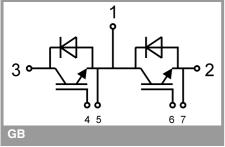


Fig. 8: Typ. switching times vs. gate resistor R_G









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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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