

### SEMITRANS<sup>®</sup> 3

### **IGBT4** Modules

#### SKM450GB12E4D1

#### Features\*

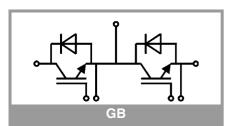
- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- Increased power cycling capability
- With integrated gate resistor
- For higher switching frequencies up to 12kHz
- UL recognized, file no. E63532
  SKM...D1: increased diode performance

#### **Typical Applications**

- AC inverter drives
- UPS

#### Remarks

- Case temperature limited to T<sub>c</sub> = 125°C max.
- Recommended  $T_{op} = -40 \dots +150^{\circ}C$
- Product reliability results valid for T<sub>i</sub> = 150°C



Absolute	e Maximum Rating	js		
Symbol	Conditions		Values	Unit
IGBT				
V <sub>CES</sub>	T <sub>j</sub> = 25 °C		1200	V
lc	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 25 °C	699	А
		T <sub>c</sub> = 80 °C	538	А
I <sub>Cnom</sub>			450	A
I <sub>CRM</sub>	$I_{CRM} = 3 \times I_{Cnom}$		1350	А
V <sub>GES</sub>			-20 20	V
t <sub>psc</sub>	$V_{CC} = 800 V$ $V_{GE} \le 15 V$ $V_{CES} \le 1200 V$	T <sub>j</sub> = 150 °C	10	μs
Tj			-40 175	°C
Inverse d	liode			•
V <sub>RRM</sub>	T <sub>j</sub> = 25 °C		1200	V
I <sub>F</sub>	T 175 00	T <sub>c</sub> = 25 °C	623	Α
Ij	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 80 °C	466	А
I <sub>Fnom</sub>			500	А
I <sub>FRM</sub>	I <sub>FRM</sub> = 2xI <sub>Fnom</sub>		1000	Α
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms, sin 180°, T <sub>j</sub> = 25 °C		2736	Α
Tj			-40 175	°C
Module	•		•	•
I <sub>t(RMS)</sub>			500	Α
T <sub>stg</sub>	module without TIM		-40 125	°C
V <sub>isol</sub>	AC sinus 50 Hz, t = 1 min		4000	V

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
IGBT							
$\begin{array}{c c} V_{CE(sat)} & I_C = 450 \text{ A} \\ V_{GE} = 15 \text{ V} \\ chiplevel \end{array}$	•	T <sub>j</sub> = 25 °C		1.84	2.07	V	
	T <sub>j</sub> = 150 °C		2.23	2.42	V		
V <sub>CE0</sub>	chiplevel	T <sub>j</sub> = 25 °C		0.80	0.90	V	
		T <sub>j</sub> = 150 °C		0.70	0.80	V	
r <sub>CE</sub>	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25 °C		2.3	2.6	mΩ	
	chiplevel	T <sub>j</sub> = 150 °C		3.4	3.6	mΩ	
V <sub>GE(th)</sub>	$V_{GE} = V_{CE}, I_{C} = 16.4 \text{ mA}$		5	5.8	6.5	V	
I <sub>CES</sub>	$V_{GE} = 0 V, V_{CE} = 12$	00 V, T <sub>j</sub> = 25 °C			5	mA	
Cies	$V_{or} = 25 V$	f = 1 MHz		27.2		nF	
Coes		f = 1 MHz		1.76		nF	
C <sub>res</sub>		f = 1 MHz		1.50		nF	
$Q_{G}$	V <sub>GE</sub> = - 8 V+ 15 V			2500		nC	
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			1.9		Ω	
t <sub>d(on)</sub>	$V_{GE} = +15/-15 V$ $R_{G on} = 1 \Omega$	T <sub>j</sub> = 150 °C		253		ns	
t <sub>r</sub>		T <sub>j</sub> = 150 °C		59		ns	
Eon		T <sub>j</sub> = 150 °C		28		mJ	
t <sub>d(off)</sub>		T <sub>j</sub> = 150 °C		505		ns	
t <sub>f</sub>		T <sub>j</sub> = 150 °C		112		ns	
E <sub>off</sub>		T <sub>j</sub> = 150 °C		58		mJ	
R <sub>th(j-c)</sub>	per IGBT				0.062	K/W	
R <sub>th(c-s)</sub>	per IGBT (λ <sub>grease</sub> =0.81 W/(m*K))			0.028		K/W	
R <sub>th(c-s)</sub>	per IGBT, pre-applied phase change material			0.017		K/W	



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- UL recognized, file no. E63532SKM...D1: increased diode
- performance

#### **Typical Applications**

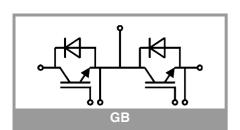
#### AC inverter drives

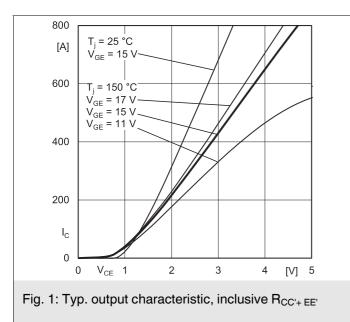
• UPS

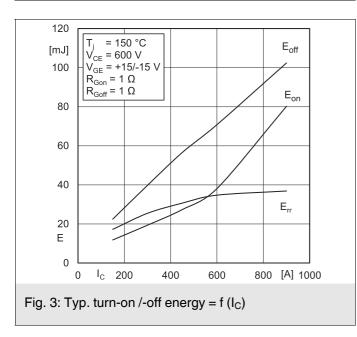
#### Remarks

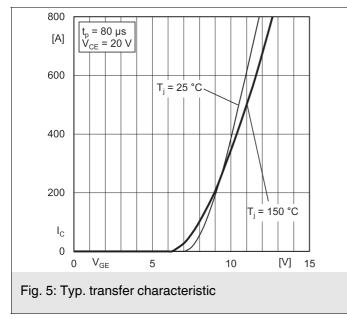
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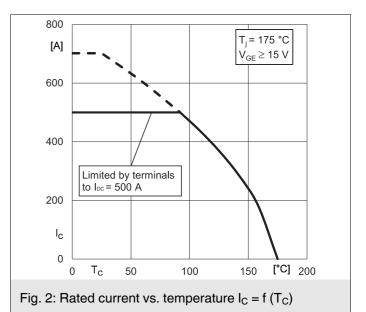
Symbol	Conditions	min.	typ.	max.	Unit	
Inverse d	iode					
$V_F = V_{EC}$	I <sub>F</sub> = 450 A V <sub>GE</sub> = 0 V chiplevel	T <sub>j</sub> = 25 °C		2.04	2.35	V
		T <sub>j</sub> = 150 °C		1.94	2.23	V
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.30	1.50	V
		T <sub>j</sub> = 150 °C		0.90	1.10	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.64	1.88	mΩ
		T <sub>j</sub> = 150 °C		2.3	2.5	mΩ
I <sub>RRM</sub>		T <sub>j</sub> = 150 °C		504		Α
Q <sub>rr</sub>		T <sub>j</sub> = 150 °C		75		μC
E <sub>rr</sub>		T <sub>j</sub> = 150 °C		31		mJ
R <sub>th(j-c)</sub>	per diode				0.095	K/W
R <sub>th(c-s)</sub>	per diode (λ <sub>grease</sub> =0.81 W/(m*K))			0.037		K/W
R <sub>th(c-s)</sub>	per diode, pre-applied phase change material			0.03		K/W
Module						•
L <sub>CE</sub>				15		nH
R <sub>CC'+EE'</sub>	measured per	T <sub>C</sub> = 25 °C		0.55		mΩ
	switch	T <sub>C</sub> = 125 °C		0.85		mΩ
R <sub>th(c-s)1</sub>	calculated without thermal coupling			0.008		K/W
R <sub>th(c-s)2</sub>	including thermal coupling, Ts underneath module $(\lambda_{grease}=0.81 \text{ W}/(\text{m}^{*}\text{K}))$			0.013		K/W
R <sub>th(c-s)2</sub>	including thermal coupling, Ts underneath module, pre-applied phase change material			0.009		K/W
Ms	to heat sink M6		3		5	Nm
Mt		to terminals M6	2.5		5	Nm
						Nm
w					325	g

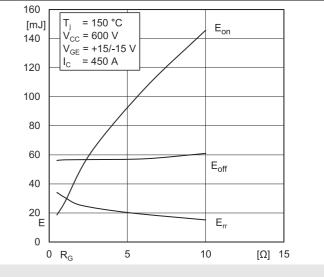


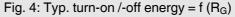


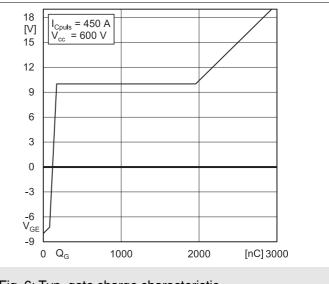


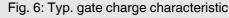


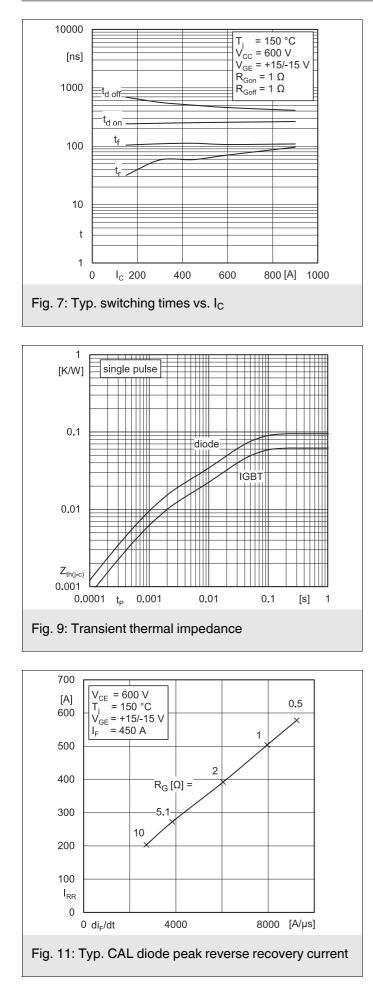












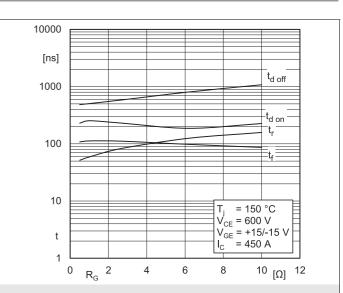
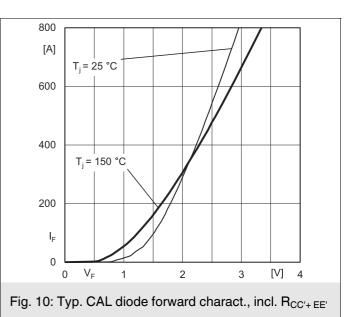
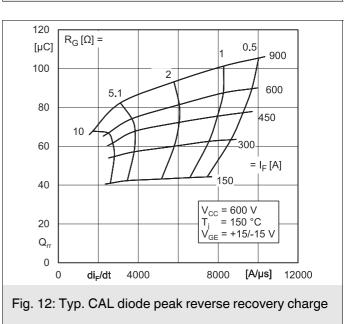
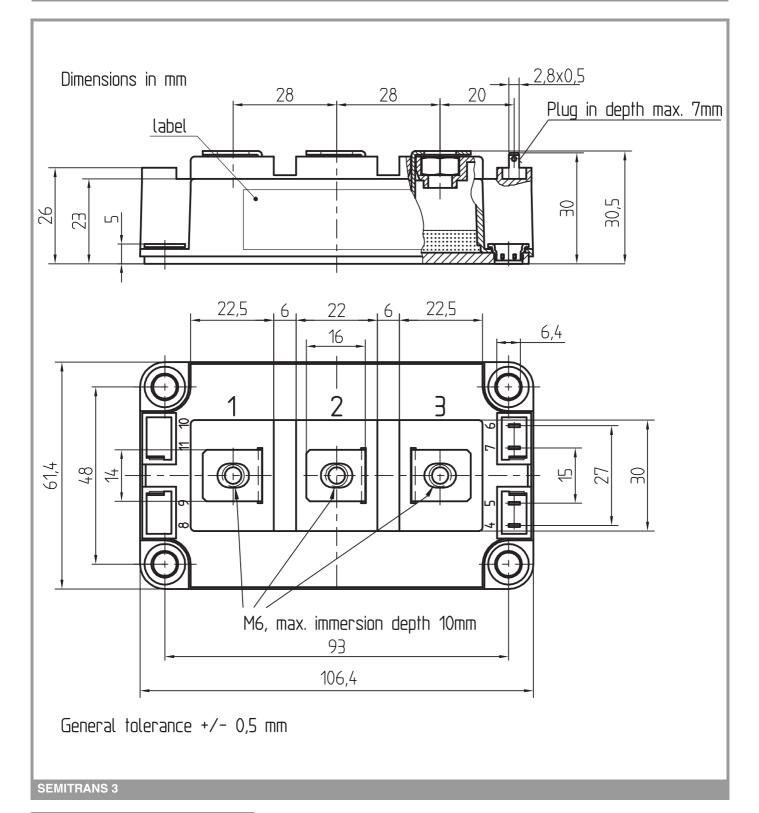
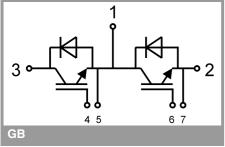


Fig. 8: Typ. switching times vs. gate resistor R<sub>G</sub>









Rev. 1.0 - 11.09.2019

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

#### **\*IMPORTANT INFORMATION AND WARNINGS**

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