

MiniSKiiP® 2 Dual

Half-Bridge

SKiiP 26GB12T7V1

Features*

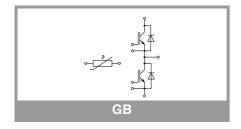
- 1200V Generation 7 IGBTs (T7)
- Robust and soft switching freewheeling diodes in CAL technology
- Highly reliable spring contacts for electrical connections
- UL recognized: File no. E63532
- NTC T-Sensor

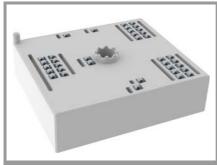
Remarks

- ·)Max. case temperature limited to TC=TS=125 °C
- Product reliability results valid for Tj≤150 °C; Tj,op >150°C during overload (Details see AN19-002)
- MiniSKiiP "Technical Explanations" and "Mounting Instructions" are part of the data sheet. Please refer to both documents for further information
- For storage and case temperature with TIM see document "Technical **Explanations Thermal Interface** Materials"

Absolute	Maximum Ratings	3		
Symbol	Conditions		Values	Unit
Inverter -	İGBT			
V _{CES}	T _j = 25 °C		1200	V
I _C	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	168	Α
	T _j = 175 °C	T _s = 100 °C	134	Α
I _C	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	212	Α
	T _j = 175 °C	T _s = 100 °C	171	Α
I _{Cnom}			200	Α
I _{CRM}			400	Α
V_{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 175 °C	7	μѕ
T _j			-40 175	°C
Inverse -	Diode			
V_{RRM}	T _j = 25 °C		1200	V
I _F	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	129	Α
	T _j = 175 °C	T _s = 100 °C	102	Α
I _F	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	148	Α
	T _j = 175 °C	T _s = 100 °C	118	Α
I _{FRM}			400	Α
I _{FSM}	$t_p = 10 \text{ ms, sin } 180^{\circ}$, T _j = 150 °C	990	Α
Tj			-40 175	°C
Module				
I _{t(RMS)}	T _{terminal} = 80 °C, 20	A per spring	200	Α
T _{stg}	module without TIM	1	-40 125	°C
V _{isol}	AC sinus 50 Hz, t =	1 min	2500	V

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Inverter - IGBT							
V _{CE(sat)}	I _C = 200 A	T _j = 25 °C		1.55	1.70	V	
	V _{GE} = 15 V	T _j = 150 °C		1.73	1.88	V	
	chiplevel	T _j = 175 °C		1.77	1.92	V	
V_{CE0}		T _j = 25 °C		1.00	1.05	V	
	chiplevel	T _j = 150 °C		0.80	0.85	V	
		T _j = 175 °C		0.75	0.80	V	
r _{CE}	V 45.V	T _j = 25 °C		2.8	3.3	mΩ	
	V _{GE} = 15 V chiplevel	T _j = 150 °C		4.7	5.2	mΩ	
		T _j = 175 °C		5.1	5.6	mΩ	
$V_{GE(th)}$	$V_{GE} = V_{CE}$, $I_C = 4.1 \text{ mA}$		5.15	5.8	6.45	V	
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	200 V, T _j = 25 °C			2.0	mA	
C _{ies}	V 05.V	f = 1 MHz		40.00		nF	
C _{oes}	$V_{CE} = 25 \text{ V}$ $V_{GE} = 0 \text{ V}$	f = 1 MHz		0.51		nF	
C _{res}		f = 1 MHz		0.14		nF	
Q_G	V _{GE} = - 8V + 15 V			2800		nC	
R _{Gint}	T _j = 25 °C			0.8		Ω	





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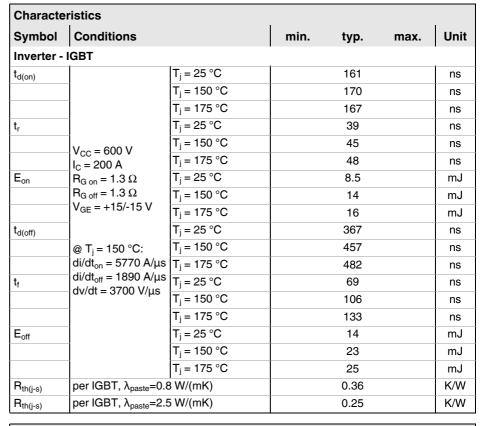
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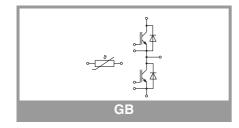
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Characte	eristics							
Symbol	Conditions		min.	typ.	max.	Unit		
Inverse - Diode								
$V_F = V_{EC}$	I _F = 200 A	T _j = 25 °C		2.20	2.52	V		
	$V_{GE} = 0 V$	T _j = 150 °C		2.15	2.47	V		
	chiplevel	T _j = 175 °C		2.00	2.31	V		
V_{F0}		T _j = 25 °C		1.30	1.50	V		
	chiplevel	T _j = 150 °C		0.90	1.10	V		
		T _j = 175 °C		0.82	0.98	V		
r _F		T _j = 25 °C		4.5	5.1	mΩ		
	chiplevel	T _j = 150 °C		6.3	6.9	mΩ		
		T _j = 175 °C		5.9	6.7	mΩ		
I _{RRM}		T _j = 25 °C		191		Α		
		T _j = 150 °C		242		Α		
	$I_F = 200 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$	T _j = 175 °C		282		Α		
Q _{rr}		T _j = 25 °C		13		μC		
		T _j = 150 °C		33		μC		
	@ T _i = 150 °C:	T _j = 175 °C		32		μC		
E _{rr}	di/dt _{off} = 5670 A/μs	T _j = 25 °C		5.5		mJ		
		T _j = 150 °C		13		mJ		
		T _j = 175 °C		16		mJ		
R _{th(j-s)}	per Diode, λ _{paste} =0.	8 W/(mK)		0.44		K/W		
R _{th(j-s)}	per Diode, λ _{paste} =2.	5 W/(mK)		0.36		K/W		
Module	•							
L _{CE}				20		nH		
Ms	to heat sink		2		2.5	Nm		
w				50		g		





Characteristics								
Symbol	Conditions	min. typ. max.		Unit				
Temperati	ure Sensor							
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)		493 ± 5%		Ω			
B _{100/125}	$R_{(T)}=R_{100}exp[B_{100/125}(1/T-1/T_{100})];T[K];$	3550 ±2%		K				

Half-Bridge

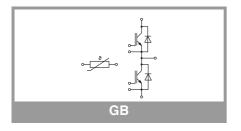
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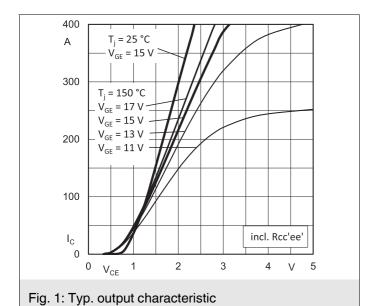
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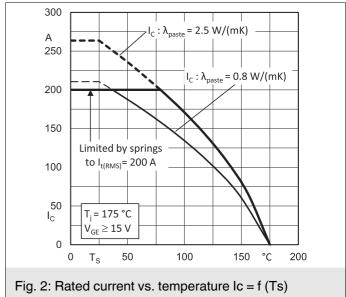
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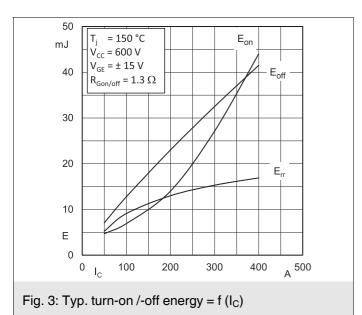
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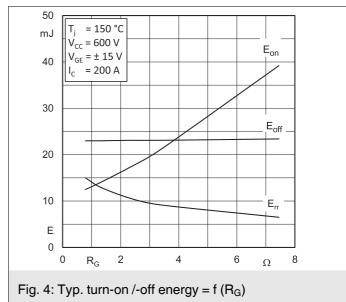
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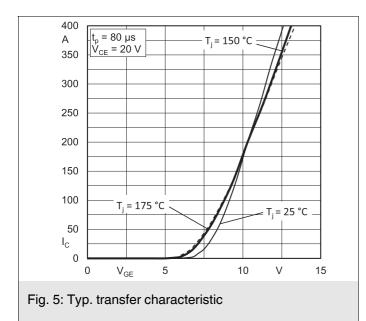


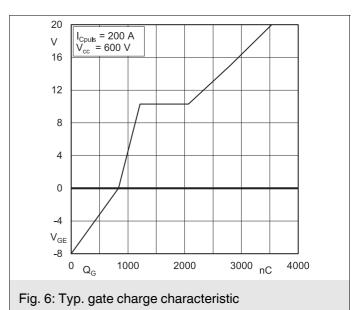












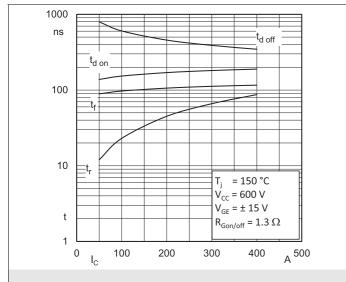


Fig. 7: Typ. switching times vs. I_C

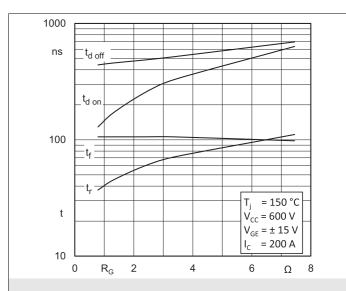


Fig. 8: Typ. switching times vs. gate resistor R_G

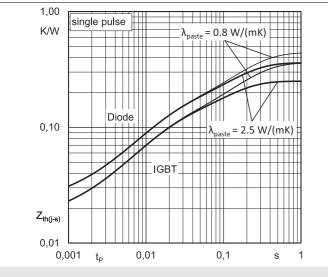


Fig. 9: Typ. transient thermal impedance

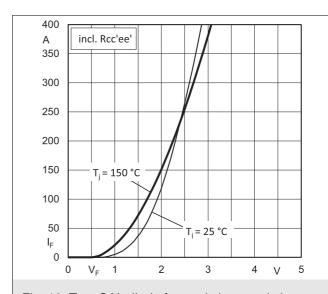


Fig. 10: Typ. CAL diode forward characteristic

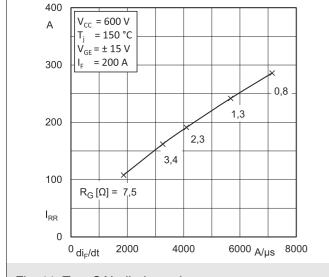


Fig. 11: Typ. CAL diode peak reverse recovery current

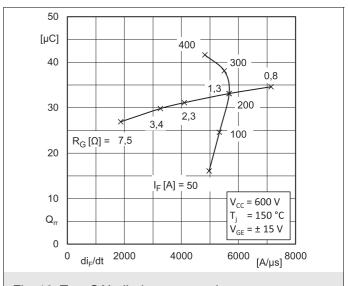
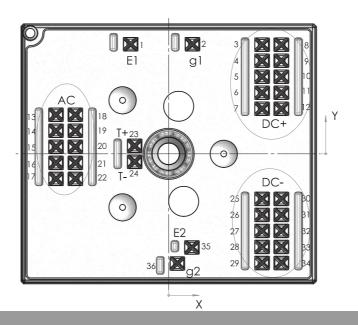


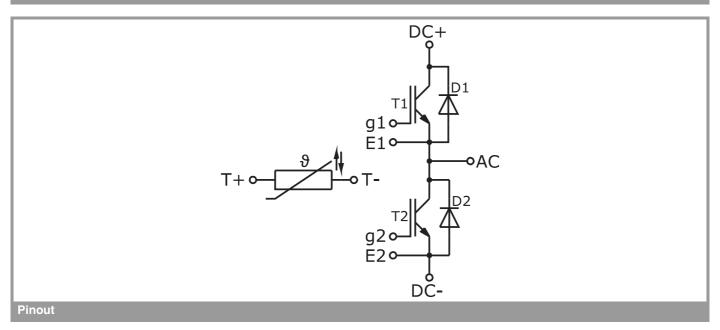
Fig. 12: Typ. CAL diode recovery charge

Pin out							
Pin	X	Y	Function	Pin	X	Υ	Function
1	-7,58	21,9	E1	19	-18,62	4,6	AC
2	4,72	21,9	g1	20	-18,62	1,4	AC
3	18,62	21,8	DC+	21	-18,62	-1,8	AC
4	18,62	18,6	DC+	22	-18,62	-5	AC
5	18,62	15,4	DC+	23	-6,78	1,6	T+
6	18,62	12,2	DC+	24	-6,78	-1,6	T-
7	18,62	9	DC+	25	18,62	-9	DC-
8	22,48	21,8	DC+	26	18,62	-12,2	DC-
9	22,48	18,6	DC+	27	18,62	-15,4	DC-
10	22,48	15,4	DC+	28	18,62	-18,6	DC-
11	22,48	12,2	DC+	29	18,62	-21,8	DC-
12	22,48	9	DC+	30	22,48	-9	DC-
13	-22,48	7,8	AC	31	22,48	-12,2	DC-
14	-22,48	4,6	AC	32	22,48	-15,4	DC-
15	-22,48	1,4	AC	33	22,48	-18,6	DC-
16	-22,48	-1,8	AC	34	22,48	-21,8	DC-
17	-22,48	-5	AC	35	4,62	-18,7	E2
18	-18,62	7,8	AC	36	1,72	-21,9	g2

all values in mm



Pinout and Dimensions



This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

*IMPORTANT INFORMATION AND WARNINGS

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